INSTRUCTION MANUAL

LOCK-IN AMPLIFIER

MODEL JB-5

SERIAL NO. 429

LOCK-IN AMPLIFIER

MODEL JB-5

INSTRUCTION MANUAL

PRINCETON APPLIED RESEARCH CORPORATION
POST OFFICE BOX 565
PRINCETON, NEW JERSEY

CONTENTS

		PAGE
I.	SPECIFICATIONS	1
II.	PRELIMINARY INSTRUCTIONS	5
III.	OPERATING INSTRUCTIONS	12
IV.	CIRCUIT DESCRIPTION	36
V.	ALIGNMENT PROCEDURE	53
VI.	WARRANTY	59
VII.	SCHEMATIC DIAGRAMS	Rear Jacket

Copyright 1963
Princeton Applied Research Corporation
Princeton, New Jersey

All rights reserved. Contents of this publication may not be reproduced in any form without written permission of the copyright owner.

I. SPECIFICATIONS

GENERAL:

FREQUENCY RANGE:

1.5 cps to 150 kc in five ranges. Calibration accuracy ± 5%.

GAIN: (RMS AC in to push-pull DC out) Greater than 9,000.

NOISE REJECTION:

A signal 47 db below ambient white noise in a 1 kc band width centered about signal frequency may be recovered with signal to noise ratio of 1.

EQUIVALENT BAND WIDTH:

0.025 cps minimum (10 seconds maximum RC integrating time) with internal capacitors. External capacitors can be used to achieve longer integrating times and narrower bandwidths. The roll-off of the output filter may be set at either 6 or 12 db/octave by means of a front panel switch (even when external filtering capacitors are used).

HARMONIC REJECTION:

Second harmonic down by factor of 1000 or 60 db. Third harmonic down by factor of 200 or 46 db.

OUTPUT:

± 5 volts D.C. maximum, balanced to ground, in series with approximately 5 k or 10 k (depending on selected recorder output). Will drive any 1 milliamp full scale pen recorder with less than 2 k internal impedance such as Esterline Angus or Texas Instrument Recti/Riter. Also direct reading panel meter.

POWER REQUIREMENTS:

105-125 volts or 210-250 volts; 50-60 cps.

SIGNAL CHANNEL CHARACTERISTICS:

INPUT IMPEDANCE:

Approximately 50 k.

INPUT LEVEL CONTROL:

Input signal may be attenuated up to a factor of 1000 in steps of .5, .2, .1, .05, .02, .01, .005, .002, .001; accuracy ± 5%.

FREQUENCY SELECTIVE AMPLIFIER:

Selectivity characteristic of tuned amplifier in signal channel is that of parallel resonant circuit with a Q of approximately 25.

REFERENCE CHANNEL CHARACTERISTICS:

The reference signal, by which the signal to be measured is demodulated, is obtained by three modes of operation; INTERNAL, EXTERNAL and SELECTIVE EXTERNAL.

SELECTIVE EXTERNAL MODE:

Externally generated reference signal filtered by tuned amplifier, having characteristics of parallel resonant circuit with a Q of approximately 25, phase shifted and applied directly to demodulator. Minimum of 100 millivolts peak to peak reference signal required.

EXTERNAL MODE:

Externally generated reference signal phase shifted and applied directly to demodulator. 5 volts peak to peak reference signal required.

INTERNAL MODE:

Self contained oscillator drives demodulator (through phase shifter) directly. Output frequency appears as sine waves at "REFERENCE IN/OUT" terminals. Maximum output 5 volts peak to peak at 3 m.a. maximum.

OUTPUT:

LEVEL:

Maximum D.C. output
a) into high impedance load ± 5 volts
b) into pen recorder of less than 2 k resistance ± 1/2 milliamp or ± 1 milliamp (switch selectable)

OVERLOAD INDICATOR:

Indicator lamp on front panel lights whenever level (signal plus noise) at mixer input exceeds linear range of mixer.

COMMON MODE QUIESCENT VALUE:

-8 volts D.C.

MONITOR:

A five position switch allows the panel meter and monitor output terminals to be switched to SIGNAL, REFERENCE, OFF and two output points in the instrument in OUT X1 and OUT X10 for level monitoring, setting up, zeroing, etc. The meter indicates peak to peak A.C. voltage when it is switched to REFERENCE and SIGNAL positions.

LINEARITY:

Better than ± 1% of full scale.

ZERO DRIFT:

Less than ± 1/2% of full scale/hour.

REFERENCE CHANNEL CHARACTERISTICS:

The reference signal, by which the signal to be measured is demodulated, is obtained by three modes of operation; INTERNAL, EXTERNAL and SELECTIVE EXTERNAL.

SELECTIVE EXTERNAL MODE:

Externally generated reference signal filtered by tuned amplifier, having characteristics of parallel resonant circuit with a Q of approximately 25, phase shifted and applied directly to demodulator. Minimum of 100 millivolts peak to peak reference signal required.

EXTERNAL MODE:

Externally generated reference signal phase shifted and applied directly to demodulator. 5 volts peak to peak reference signal required.

INTERNAL MODE:

Self contained oscillator drives demodulator (through phase shifter) directly. Output frequency appears as sine waves at "REFERENCE IN/OUT" terminals. Maximum output 5 volts peak to peak at 3 m.a. maximum.

OUTPUT:

LEVEL:

Maximum D.C. output
a) into high impedance load ± 5 volts
b) into pen recorder of less than 2 k resistance ± 1/2 milliamp or ± 1 milliamp (switch selectable)

OVERLOAD INDICATOR:

Indicator lamp on front panel lights whenever level (signal plus noise) at mixer input exceeds linear range of mixer.

COMMON MODE QUIESCENT VALUE:

-8 volts D.C.

MONITOR:

A five position switch allows the panel meter and monitor output terminals to be switched to SIGNAL, REFERENCE, OFF and two output points in the instrument in OUT X1 and OUT X10 for level monitoring, setting up, zeroing, etc. The meter indicates peak to peak A.C. voltage when it is switched to REFERENCE and SIGNAL positions.

LINEARITY:

Better than ± 1% of full scale.

ZERO DRIFT:

Less than ± 1/2% of full scale/hour.

ponent in the noise generated by transistors (and tubes for that matter). In general the noise figure of a transistor amplifier begins to deteriorate below about 100 cps. The internal noise level of the JB-5 at the extreme low end of its range is somewhat higher than at say 400 cps. However, it will be observed that with a long filter time constant (eg. 10 seconds) in use the output noise is at an acceptable level. Since lock-in operation at these low frequencies always entails the use of time constants of this order it is only noise output under such conditions that is meaningful. A careful check of the "A.C. Balance" adjustment should be performed to make certain that mixer unbalance is not contributing to output fluctuations seen at low frequencies without filtering. If sufficient gain is provided ahead of the JB-5 it will be possible to swamp its internal noise with noise accompanying the signal at any frequency. The user should carefully examine his input signal level to determine his requirements for preamplification at any operating frequency.

II. PRELIMINARY INSTRUCTIONS

COOLING:

Because of the low total power consumption of the instrument its case is unventilated. At installation the only precaution to be observed is to insure that the ambient temperature in which it is operated will not exceed 45°C (113°F). This will insure trouble-free continuous operation.

POWER REQUIREMENTS:

The instrument requires approximately 10 watts at either 105 to 125 V. A.C. or 210 to 250 V. A.C.; 50 to 60 cycles. It is normally shipped wired for 105 to 125 V. operation. If it is shipped wired for 210 to 250 V. operation, the line cord will bear a tag at the plug end so marked. Transformer primary connections for changing from 117 V. A.C. to 234 V. A.C. or vice versa are illustrated in Figure 1 and the procedure for making this change is outlined below.

1. Remove the top, bottom and back sheet metal covers from the instrument (Each of these is held by 6 screws).

2. Remove the right (viewed from the front) side panel by removing the 4 screws securing it to the printed circuit board assembly, and the 2 screws securing it to the front panel. Easy access to the power transformer may now be had from the right side of the instrument.

3. The transformer primary connections for 117 V. A.C. operation are shown in Figure 1A.

4. The transformer primary connections for 234 V. A.C. operation are shown in Figure 1B. The instrument case can be reassembled by reversing the above procedure.

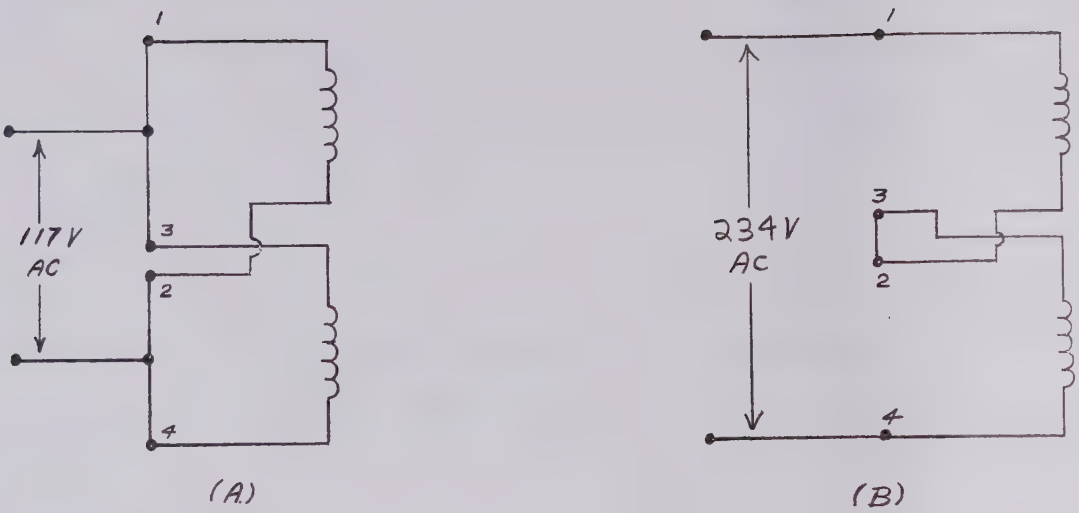


FIGURE 1

TRANSFORMER PRIMARY CONNECTIONS FOR OPERATION FROM EITHER 117 V A.C. OR 234 V A.C.

FUSING:

A 1/4 A. slo-blo 3AG 250 V. fuse is supplied, and gives adequate protection to the instrument in either 105 to 125 V. operation or in 210 to 250 V. operation at either 50 or 60 cps.

FRONT PANEL CONTROLS AND CONNECTIONS

POWER:

In the "OFF" position this switch completely disconnects the power line from the instrument.

SIGNAL LEVEL:

This control sets the fraction of the voltage applied to the "SIGNAL INPUT" terminals which is applied to the input of the signal channel tuned amplifier.

SIGNAL INPUT

These terminals are connected to the input of the signal channel tuned amplifier via the "SIGNAL LEVEL" control. This input is single ended. The red binding post is the "high" side; the black binding post is connected to

chassis ground. A resistance of 54 K ± 5% (the resistance of the signal level control) is internally connected directly across these terminals.

CAUTION: THE VOLTAGE (D.C. + PEAK A.C.) APPLIED BETWEEN THESE TERMINALS SHOULD NEVER EXCEED ± 25 V.

FREQUENCY:

This dial simultaneously tunes the reference and signal channel tuned amplifiers. The reference frequency in cps is the reading on the dial times the reading on the "FREQ. RANGE" switch. The accuracy of this frequency setting is ± 5%.

FREQUENCY RANGE:

This switch selects the range which can be continuously tuned by the "FREQUENCY" dial. In the "X1" position the range is 1.5 cps to 15 cps; in "X10", 15 cps to 150 cps; in X100, 150 cps to 1.5 kc; in X1K, 1.5 kc to 15 kc and in X10K, 15 kc to 150 kc. This setting applies to both signal and reference channels.

FREQUENCY TRIM:

This control varies the frequency of the signal channel tuned amplifier only. Total variation is approximately ± 5%. It is used to peak the signal channel at any given frequency setting, thus compensating for possible errors in tracking between the signal and reference tuned amplifiers.

PHASE:

This control varies the phase of the reference relative to the signal. It provides a continuously variable shift of at least 180° at any frequency within the range of the instrument.

REFERENCE MODE:

This switch is used to select one of the three modes of reference operation. (See "OPERATING INSTRUCTIONS" for a detailed description of the three possible

modes of operation).

REFERENCE LEVEL:

In the ("EXT" / "SEL EXT.") mode, this control sets the fraction of the voltage applied to the "REFERENCE IN/OUT" terminals which reaches the input of the (phase shifter / reference channel tuned amplifier). In the "INT." mode it controls the level of the sinusoidal output voltage appearing at the "REFERENCE IN/OUT" terminals.

REFERENCE IN/OUT:

In the "EXT." reference mode these terminals connect to the input of the phase shifter via the "REFERENCE LEVEL" control. In the "SEL. EXT." mode these terminals connect to the input of the reference channel tuned amplifier via the "REFERENCE LEVEL" control. In either case the red binding post is the "high" side of the single-ended input, the black binding post being connected to chassis ground. In either mode a resistance of 54 K ($\pm$ 5%) (the resistance of the "REFERENCE LEVEL" control) is connected directly across these terminals.

In the "INT." reference mode a sine wave signal at the reference frequency appears at these terminals. Its amplitude is set by the "REFERENCE LEVEL" switch. With this switch in the "1" position the sine wave output is approximately 5 V. peak to peak. In any other position the output is the fraction of this value indicated by the "REFERENCE LEVEL" control. The output from these terminals is single-ended, the red binding post being the "high" side and the black binding post being chassis ground. The impedance seen looking into these terminals varies with the "REFERENCE LEVEL" setting. It has a maximum value of approximately 1.4 K when this switch is in the "0.5" position.

FILTER:

This switch determines the time constant of the RC low-pass filter at the output of the mixer. The numbers at each switch position give this time

constant in seconds. In the "ZERO" position there is no internal low pass filtering at all. In the "EXT." position the filtering time constant is determined by external capacitors.

6 DB - 12 DB PER OCTAVE SWITCH:

This switch sets the roll-off of the RC low pass filter at the mixer output to either "6 DB/OCTAVE" (single RC filtering) or "12 DB/OCTAVE" (double RC filtering). In either case the time constant of the filtering is as set on the "FILTER" switch. This switch is also operative when external capacitors are used.

OUTPUT:

This group of 3 terminal is provided to drive a galvanometric recorder having a 0-1 mA movement and an internal resistance of less than 2K. When the panel meter (with the "METER/MONITOR" switch in "OUT X1" or "OUT X10" position) reads zero, the two red binding posts are at the same potential and are both at approximately -8V relative to chassis ground. The impedance seen looking into these two terminals is approximately $\begin{pmatrix} 5K \\ 10K \end{pmatrix}$ when the recorder switch on the rear panel is in $\begin{pmatrix} 1mA \\ 1/2mA \end{pmatrix}$ position. The black binding post is chassis ground.

INVERT:

This switch inverts the phase (i.e. produces a 180° phase change) of the reference. It has the effect of inverting the sense of the mixer output voltage.

ZERO:

This control sets the quiescent D.C. level of the output. It may be used to set the quiescent reading of the panel meter and (if used) to set the pen recorder to zero with no input applied to the instrument.

METER/MONITOR SWITCH:

This switch performs two functions. It determines the internal point in the instrument monitored by the panel meter and simultaneously connects the

same point to the "MONITOR" terminals for oscilloscope monitoring.

1. Panel meter: In the "SIG." position of the "METER/MONITOR" switch the meter indicates the output level of the signal channel tuned amplifier. In the "REF." position it indicates the output level of the phase-shifter. In the "OFF" position the panel meter is completely disconnected. In the "OUT X1" position the panel meter reads the output of the D.C. amplifier following the mixer, full scale being 5 V. D.C. In the "OUT X10" position the panel meter reads the output of the D.C. amplifier, full scale in this case being 0.5 V. D.C.

2. Monitor terminals. In the "SIG." position of the "METER/MONITOR" switch the output of the signal channel tuned amplifier is available at these terminals. In the "REF" position the output of the phase-shifter appears at these terminals. In the "OFF", "OUT X1" and "OUT X10" positions the output of the D.C. amplifier appears at these terminals.

Notes: 1) The function of the "OUTPUT" terminals is unaffected by this switch.

2) It is undesirable to operate the instrument with the "METER/MONITOR" switch in the "SIG." or "REF." positions as the loading due to the meter circuit produces unbalances in the amplifiers being monitored.

MONITOR TERMINALS:

This group of 3 terminals provides for oscilloscope monitoring of all important internal wave forms and also provides a low-impedance voltage output from the instrument in cases where such is desired. The waveform appearing at these terminals depends on the setting of the "METER/MONITOR" switch.

1. "SIG." position: In this case the two red binding posts are connected to the two outputs (push-pull) of the signal channel tuned amplifier. The

upper one is at a D.C. level of approximately -5V. and the lower at approximately -15V., relative to chassis ground.

2. "REF." position: In this position the upper red binding post is connected to the output (single-ended) of the phase-shifter. The lower one is connected to chassis ground.

3. In the "OFF", "OUT X1" and "OUT X10" positions of the "METER/MONITOR" switch the two red binding posts are connected to opposite ends of the output (push-pull) of the DC amplifier following the mixer. When the panel meter reads zero (in "OUT X1" or "OUT X10" positions) the voltage between these binding posts is zero. In this case both binding posts are at approximately -8V. relative to chassis ground. When the D.C. amplifier is delivering maximum output the D.C. voltage appearing between these terminals is $\pm$ 5 V (corresponds to $\pm$ full scale reading on panel meter with "METER/MONITOR" switch in "OUT X1" position). The voltage appearing between these terminals is the same in any of the last three ("OFF", "OUT X1" or "OUT X10") positions of the "METER/MONITOR" switch.

The black binding post is connected to chassis ground in any position of the "METER/MONITOR" switch.

III. OPERATING INSTRUCTIONS

GENERAL: The PAR Model JB-5 Lock-In Amplifier is essentially a detection system capable of operating with an extremely narrow equivalent noise band width. Its function is to select a band of frequencies from a signal spectrum applied to its input and to convert the information therein to an equivalent band width at D.C. The center frequency of the band so selected is equal to the reference frequency being used.

Figure 2 is a block diagram of the instrument. The signal containing the information to be detected is applied to the "SIGNAL INPUT" terminals. An appropriate fraction of this signal is picked off by the "SIGNAL LEVEL" control and applied to the input of the signal channel tuned amplifier. The role of this tuned amplifier is twofold:

1. By limiting the bandwidth at the detector input it increases the percentage of the peak input voltage to the detector which is due to the desired signal.

2. It attenuates components in the signal spectrum at the odd harmonics of the reference frequency to which the phase-sensitive detector is sensitive. (This sensitivity to odd harmonics of the reference frequency is due to the use of a square wave switching type phase sensitive detector).

The output of the signal channel tuned amplifier is applied to the input of the phase-sensitive detector. This is essentially a device which mixes the signal and reference frequencies so as to produce corresponding sum and difference frequencies at its output. A low-pass filter at the output of the phase detector rejects the high frequency components corresponding to sum frequencies, and passes those of the difference frequencies that lie within its pass-band. In particular the difference frequency due to components of the

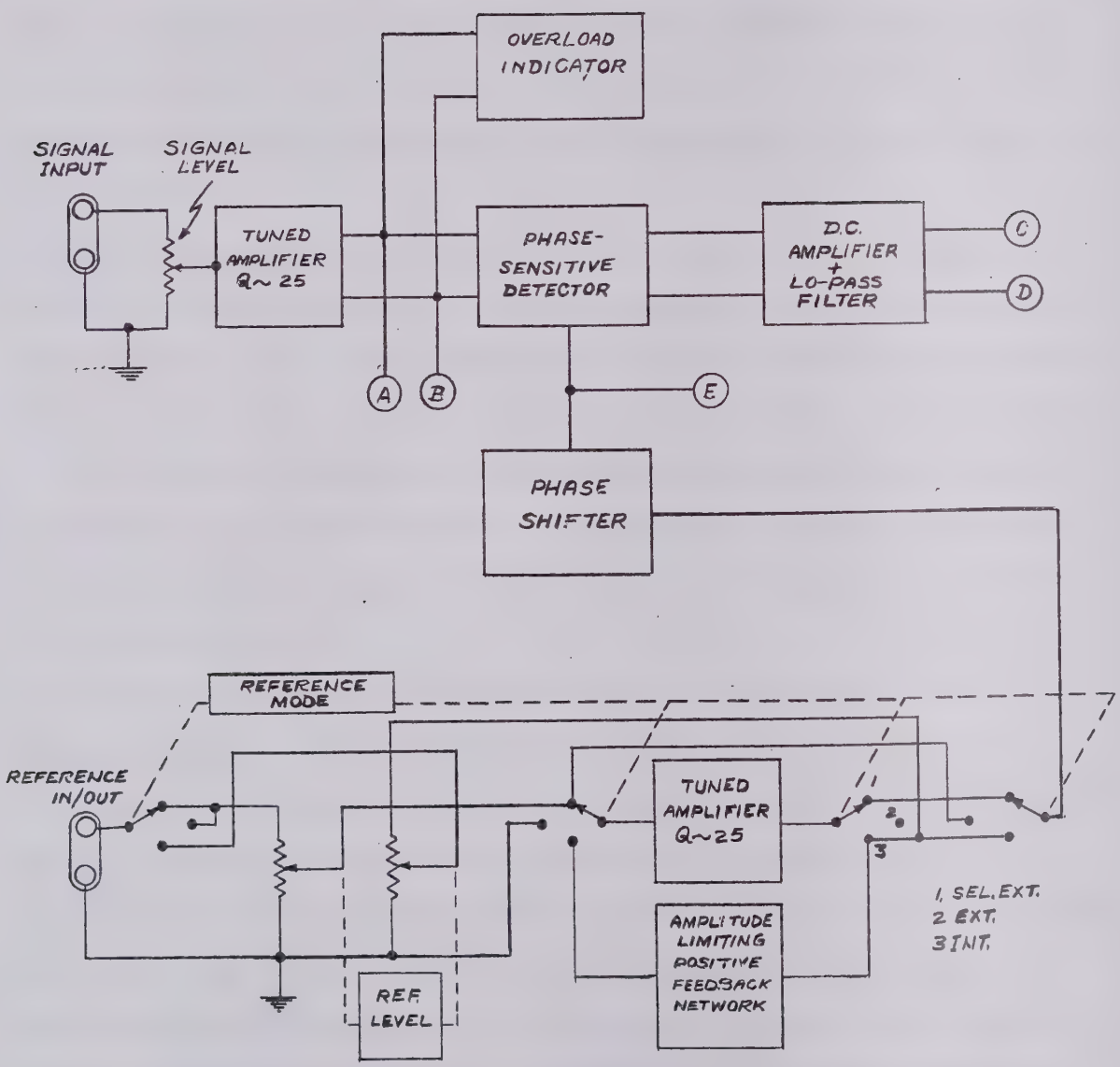


FIGURE 2

BLOCK DIAGRAM MODEL JB-5 LOCK-IN AMPLIFIER

signal at the reference frequency is D.C. Components of the signal spectrum at frequencies differing from the reference frequency by more than the cut-off frequency of the low pass filter will produce difference frequencies which will be attenuated by it. Consequently the output from the low pass filter will be due to that portion of the signal spectrum which lies within a band of frequencies about the reference frequency determined by the pass band of the low pass filter.

The reason a mixer of this type is called a phase sensitive detector is that the D.C. output produced by it due to a signal at the reference frequency is proportional to the cosine of the relative phase of the signal and reference. This property is made explicit use of in some lock-in amplifier applications.

The reference frequency for this instrument may be generated internally or obtained from an external source. In either case a variable phase-shifter is available in order to allow the relative phase of the signal and reference to be adjusted as desired.

A detailed description of the operation of the instrument follows:

REFERENCE CHANNEL: The operation of the reference channel depends on the mode being used. Consequently the three available modes are treated separately.

EXTERNAL: This mode is used when a sine wave of at least 5V peak to peak is available for reference (at this level the mixer is insensitive to small changes in reference amplitude). If a reference waveform meeting these requirements is available it can be coupled into the instrument via the "REFERENCE IN/OUT" terminals located directly below the "REFERENCE LEVEL" control on the front panel. This input is single-ended, the red binding post being the "high" side and the black one being chassis ground. A resistance of 54K ($\pm$ 5%) (the "REFERENCE LEVEL" control) is internally connected directly across these terminals (see FIG. 2). In this mode of operation the only adjustment required

is to set the reference amplitude at the proper value. With the instrument power on and the reference source output connected to the "REFERENCE IN/OUT" terminals, set the "REFERENCE LEVEL" control at "OFF", the "REFERENCE MODE" switch to "EXT." and the "METER/MONITOR" switch to "REF." Connect the input of an oscilloscope to the upper of the two red "MONITOR" terminals and the ground on the oscilloscope to the black terminal. Rotate the "REFERENCE LEVEL" control until the amplitude of the sine wave seen on the oscilloscope is between 4 and 6 volts peak to peak. Note the reading on the panel meter. This reading corresponds to a proper reference level for your instrument and may be used for checking and resetting (NOTE: a slight flattening of the positive peak of the sine wave may be observed. This is in no way harmful to the operation of the instrument).

The instrument is now set up for operation in the "EXT" reference mode.

SELECTIVE EXTERNAL MODE: In any reference mode the reference signal is passed through the phase-shift network before being applied to the detector. The detector is essentially a double-pole double-throw switch which is driven at the reference frequency. A square wave is required to drive the switch. This square wave is obtained by feeding the output of the phase shifter into a Schmitt trigger circuit. Certain non-sinusoidal waveforms applied to the input of the phase shifter will produce output wave forms which will result in improper triggering of the Schmitt and consequent improper operation of the detector. For this reason if the reference wave form is not sinusoidal it should be passed through the reference channel tuned amplifier in order to make it more nearly sinusoidal by reducing its harmonic content before applying it to the phase shifter. In general, this mode of operation allows a particular frequency component to be selected out of a complex waveform and used as the reference frequency.

With the instrument power on connect the output of the reference source to the "REFERENCE IN/OUT" terminals. This input is single-ended, the red binding

post being the "high" side and the black one being chassis ground. A resistance of 54 K ($\pm$ 5%) is internally connected directly across these terminals (the "REFERENCE LEVEL" control) (See FIG. 2). Set the "REFERENCE MODE" switch to "SEL. EXT.", the "REFERENCE LEVEL" control to "OFF", and the "METER/MONITOR" switch to "REF.". Set the "FREQ. RANGE" switch and "FREQUENCY" dial to the desired reference frequency. Connect the upper red "MONITOR" terminal to the input of an oscilloscope, and the oscilloscope ground to the black terminal.

Turn the "REFERENCE LEVEL" control clockwise until a sine-wave appears on the scope. Adjust the "FREQUENCY" control so as to maximize the amplitude of this sine wave. Adjust the "REFERENCE LEVEL" control so that the amplitude of the maximum response so obtained is 4 to 6 V. peak to peak as measured by the scope. (A slight flattening of the positive peaks of the sine wave may be observed; this in no way effects proper operation of the instrument).

If the desired frequency component of the reference wave form contributes only a small percentage of the peak amplitude of the waveform it may not be possible to obtain a sine wave of the amplitude specified above, due to the peak amplitude of the reference waveform exceeding the limits of the tuned amplifier. If this is the case the only solution is, by means of external filtering, to reduce the amplitude of the undesired frequencies in the reference waveform before applying it to the input of the tuned amplifier.

Once a sinusoidal waveform within the above amplitude limits has been obtained at the "MONITOR" terminals, note the reading of the panel meter. This reading corresponds to the proper reference level for your instrument and can be used for re-setting and checking.

The above adjustments having been carried out, the instrument is ready for operation in the "SEL. EXT." reference mode.

INTERNAL MODE: In general, operation in this mode requires no adjustments.

However, in the case of a new instrument the operation in this mode should be checked to make sure it is proper before the instrument is used. To do this, turn the instrument power on, set the "REFERENCE MODE" switch to "INT." and the "METER/MONITOR" switch to "REF." A reading should be observed on the panel meter of approximately 75% of full scale. Connect the upper red "MONITOR" terminal to the input of an oscilloscope; connect the scope ground to the black terminal. With the "FREQUENCY" dial set to 4.0 and the "FREQ. RANGE" switch to "X100" (i.e. 400 cps reference frequency) the amplitude of the sine wave appearing on the scope should be between 4 and 6 V. peak to peak (5 V. nominal). If this is not the case, refer to the internal oscillator section in "ALIGNMENT PROCEDURE" and follow the alignment instructions therein.

If the level observed on the scope is within the limits specified above the instrument is ready for use in the "INT." reference mode. Note the reading on the panel meter; this reading represents a proper reference level for your instrument. The level indicated by the meter will vary somewhat with frequency due to variation in the Q of the reference channel tuned amplifier with frequency. However, if adjusted as recommended at 400 cps this level will remain within acceptable limits over the entire frequency range of the instrument.

In this mode of operation a sinusoidal output is available at the "REFERENCE IN/OUT" terminals. This voltage has an amplitude of from 4 to 6 V. peak to peak and is controlled by the "REFERENCE LEVEL" control (this control does not effect the meter reading in the "REF." position, or the amplitude seen on the scope.) The impedance seen looking into the terminals under these conditions varies with the "REFERENCE LEVEL" setting, being a maximum of 1.4 K in the "0.5" position. This output is single-ended, the red binding post being the "high" side and the black one being chassis ground.

SIGNAL CHANNEL: The "SIGNAL INPUT" binding posts are located directly below the "SIGNAL LEVEL" control on the left side of the panel. This input is single-

However, in the case of a new instrument the operation in this mode should be checked to make sure it is proper before the instrument is used. To do this, turn the instrument power on, set the "REFERENCE MODE" switch to "INT." and the "METER/MONITOR" switch to "REF." A reading should be observed on the panel meter of approximately 75% of full scale. Connect the upper red "MONITOR" terminal to the input of an oscilloscope; connect the scope ground to the black terminal. With the "FREQUENCY" dial set to 4.0 and the "FREQ. RANGE" switch to "X100" (i.e. 400 cps reference frequency) the amplitude of the sine wave appearing on the scope should be between 4 and 6 V. peak to peak (5 V. nominal). If this is not the case, refer to the internal oscillator section in "ALIGNMENT PROCEDURE" and follow the alignment instructions therein.

If the level observed on the scope is within the limits specified above the instrument is ready for use in the "INT." reference mode. Note the reading on the panel meter; this reading represents a proper reference level for your instrument. The level indicated by the meter will vary somewhat with frequency due to variation in the Q of the reference channel tuned amplifier with frequency. However, if adjusted as recommended at 400 cps this level will remain within acceptable limits over the entire frequency range of the instrument.

In this mode of operation a sinusoidal output is available at the "REFERENCE IN/OUT" terminals. This voltage has an amplitude of from 4 to 6 V. peak to peak and is controlled by the "REFERENCE LEVEL" control (this control does not effect the meter reading in the "REF." position, or the amplitude seen on the scope.) The impedance seen looking into the terminals under these conditions varies with the "REFERENCE LEVEL" setting, being a maximum of 1.4 K in the "0.5" position. This output is single-ended, the red binding post being the "high" side and the black one being chassis ground.

SIGNAL CHANNEL: The "SIGNAL INPUT" binding posts are located directly below the "SIGNAL LEVEL" control on the left side of the panel. This input is single-

2. Reduce the level of the spurious components accompanying the signal by means of external filtering before applying the signal to the input terminals of the lock-in amplifier. Once a satisfactory adjustment is obtained as outlined above, a final oscilloscope check is desirable. With the "METER/MONITOR" switch in the "SIG." position, the output of the signal channel tuned amplifier (points A and B in Figure 2) is available at the "MONITOR" terminals. A single-ended scope input may be connected between either red terminal and the black ground terminal. If desired, a balanced input scope may be connected between the two red terminals. The waveform seen should be sinusoidal and free of any flattening or clipping on peaks. If such flattening is observed, the level in the tuned amplifier is too high and one of the two alternative procedures listed above should be followed to correct the situation.

This completes the preliminary adjustment of the signal channel.

PHASE SHIFTER: With the signal and reference channels operating and adjusted as described above, place the "FILTER" switch in the "ZERO" position and the "METER/MONITOR" switch in the "OUT X1" position. Connect an oscilloscope to the "MONITOR" terminals. (These terminals are now connected to points C and D in FIG. 2). A single-ended scope may be connected between either red terminal and the black ground terminal. A balanced scope input may be connected between the two red terminals. If the signal level has been set according to the above instructions a substantial deflection of the panel meter should be observed.

If the reading on the panel meter fluctuates wildly due to noise accompanying the input signal, turn the "FILTER" switch clockwise, thus narrowing the mixer bandwidth until the fluctuations are reduced to an acceptable level. If "in phase" operation of the instrument is desired, rotate the "PHASE" control until maximum average deflection of the meter is obtained. If "90° out of phase" operation is desired, adjust the "PHASE" control so that the average

reading of the meter is zero. Oscilloscope waveforms corresponding to the two cases are shown in FIG. 3. These wave forms correspond to a pure sine wave signal; in a case where the signal is accompanied by noise they may be somewhat obscured by the noise but they should be still easily recognized. These waveforms can only be observed with the "FILTER" switch in the "ZERO" position. If this results in wild excursions of the panel meter, turn the "METER/MONITOR" switch to the "OFF" position while examining these wave forms.

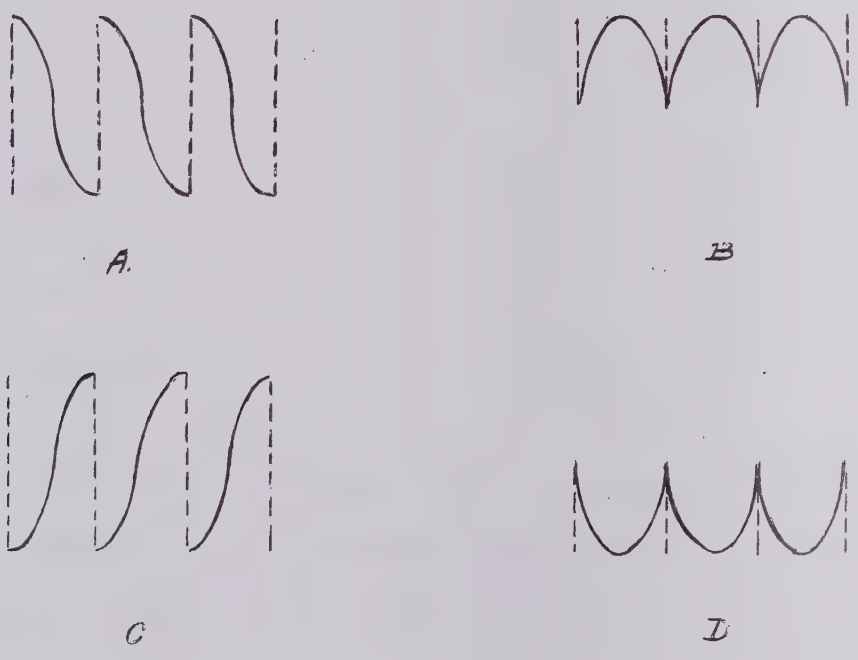
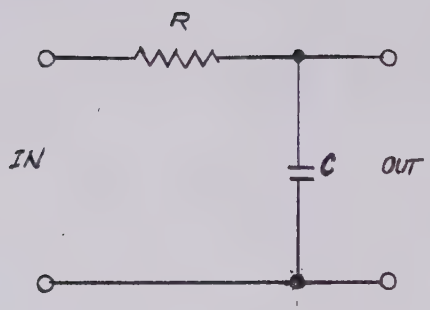
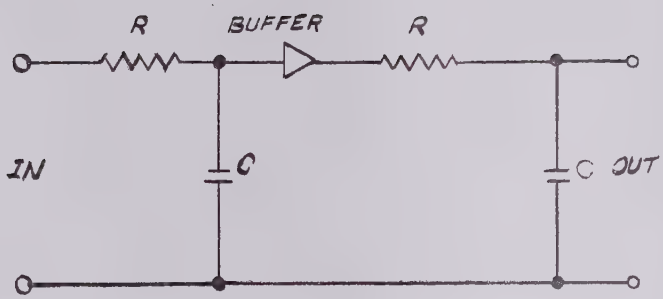


FIG. 3

MIXER OUTPUT WAVEFORMS A AND C - SIGNAL AND REFERENCE 90° OUT OF PHASE-B AND D-SIGNAL AND REFERENCE IN PHASE. THE VERTICAL LINES REPRESENT MIXER SWITCHING TIMES.

OUTPUT FILTERING: The time constant of the output RC filtering is determined by the setting of the "FILTER" switch. Internal capacitors provide "built in" time constants of: 0.001, 0.01, 0.1, 1, 3 and 10 seconds. Single-section or double-section RC filtering may be selected by the "6 db-12db/OCTAVE" switch. (6 db/octave corresponding to single RC and 12 db/octave to double RC filtering.





$\Delta B = \frac{1}{2RC}$

$T_r = 2.2RC$

FIGURE 4A

$\Delta B = \frac{1}{4RC}$

$T_r = 3.4RC$

FIGURE 4B

A word about the choice of single or double section RC filtering is in order here. Figures 4A and 4B respectively show single and double section RC filters similar to those used in the JB-5.

NOTES:

1. The actual circuits in the JB-5 look different from those in Figure 4 because of switching and the fact that they have push-pull inputs and outputs. Operationally the circuits actually used are completely equivalent to those in Figure 4.

2. The effect of the "buffer" in Figure 4B is to isolate the two sections in such a way that the second section does not load the first. In the JB-5 the D.C. amplifier plays the role of buffer.

The equivalent noise bandwidth of a single-section RC filter is given by:

$$\triangle B = \frac{1}{2RC}$$

the rise time (10% to 90%) is:

$$T_r = 2.2RC$$

For the double section filter of figure 4B we have:

$$\triangle B = \frac{1}{4RC} \text{ and } T_r = 3.4RC$$

In comparing the two we see:

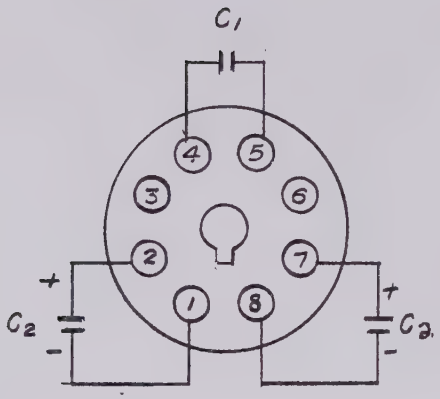
1. For a given time constant (RC) the equivalent noise bandwidth of the double-section filter is 1/2 that of the single section.

2. For a given noise bandwidth the rise time of a double section filter is about 23% less than that of a single section filter.

Whenever the JB-5 is used as a detector, driving a recorder, it is clear from the above that double-section RC filtering is a distinct advantage in providing a worthwhile decrease in response time for a given noise bandwidth.

In many applications the lock-in amplifier is used as a component in a closed "servo loop". Since one criterion of stability (freedom from oscillation) of such a loop is that at no frequency should the open loop gain roll off as fast as 12 db/octave, in such applications the 12 db/octave position cannot be used. It is for this reason that the 6 db/octave position is provided on the JB-5.

USE OF EXTERNAL CAPACITORS: A standard octal socket is provided at the rear of the JB-5 to allow those needing filtering time-constants longer than those available internally to obtain them by making use of external capacitors. When the "FILTER" switch is in the "EXT." position the filter time constant may be determined by capacitors wired to this socket as shown in Figure 5.



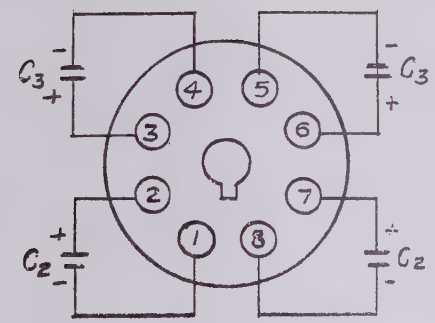


FIGURE 5A

FIGURE 5B

Figure 5 shows two optional arrangements of external capacitors. The values of the capacitors for a given filtering time constant are as follows:

Capacitors	Working Voltage
C_1, 12 Mfd/sec.	15
C_2, 6 Mfd/sec.	15
C_3, 24 Mfd/sec.	15

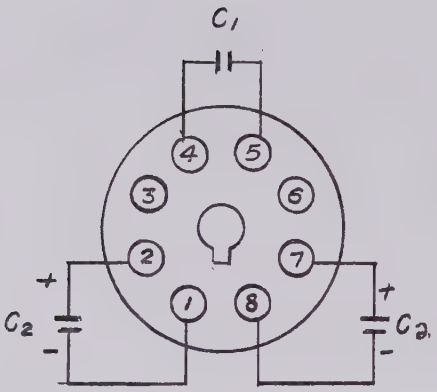
For example: If a 30 second time-constant is desired, the following would be required:

$C_1 = 12 \times 30 = 360$ Mfd.

$C_2 = 6 \times 30 = 180$ Mfd.

$C_3 = 24 \times 30 = 720$ Mfd.

That is, if the arrangement of Figure 5A were to be used, the capacitors required would be: 1@ 360 Mfd, 2@ 180 Mfd., if the arrangement of Figure 5B



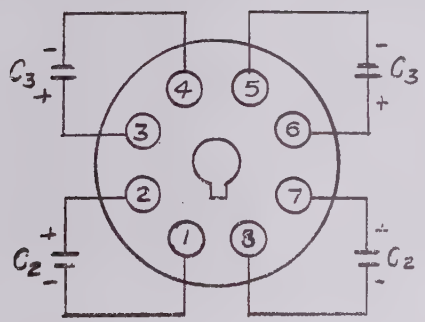


FIGURE 5A

FIGURE 5B

Figure 5 shows two optional arrangements of external capacitors. The values of the capacitors for a given filtering time constant are as follows:

Capacitors	Working Voltage
C_1, 12 Mfd/sec.	15
C_2, 6 Mfd/sec.	15
C_3, 24 Mfd/sec.	15

For example: If a 30 second time-constant is desired, the following would be required:

$C_1 = 12 \times 30 = 360$ Mfd.

$C_2 = 6 \times 30 = 180$ Mfd.

$C_3 = 24 \times 30 = 720$ Mfd.

That is, if the arrangement of Figure 5A were to be used, the capacitors required would be: 1@ 360 Mfd, 2@ 180 Mfd., if the arrangement of Figure 5B

dicator circuit has been designed so that the light will come on before a serious overload condition exists.

At this point a final oscilloscope check of the wave form at the output of the signal channel tuned amplifier is desirable in order to check for an overloaded condition in this amplifier. Any peak flattening of this waveform means the signal channel level must be reduced (see "SIGNAL CHANNEL" section).

Avoiding non-linearity due to overload is a prerogative to satisfactory operation of this (or any other) lock-in amplifier. The three possible overload conditions of the JB-5 are summarized below:

1. SIGNAL CHANNEL: discussed above and in "SIGNAL CHANNEL" section.

2. MIXER: discussed above.

3. OUTPUT D.C. AMPLIFIER: An over-loaded condition of the output D.C. amplifier is indicated by an off-scale reading of the panel meter with the "METER MONITOR" switch in the "OUT X1" position. The "cure" is simply to reduce the signal level by means of the "SIGNAL LEVEL" control.

RECORDER OPERATION: If a galvanometer type recorder such as the Esterline Angus or the Texas Instruments Recti/riter is to be used it should be connected to the two red "OUTPUT" binding posts. The lock-in amplifier will drive any such recorder of 1 ma sensitivity with a coil resistance of less than 2K.

Two modes of operation are provided for such recorders; "left hand zero" operation and "center zero" operation. A toggle switch located on the rear of the instrument selects the desired mode, and a screwdriver-adjust potentiometer, also located on the back of the instrument allows the recorder to be adjusted to agree with the panel meter.

LEFT-HAND ZERO OPERATION: In this mode of operation the recorder pen lies at the left-hand edge of the chart when the panel meter reads zero[1] and at the

right-hand edge when the panel meter reads full scale[1]. If such operation is desired, the set-up is as follows: First set the recorder pen at the left-hand chart edge by means of the mechanical zero-adjuster on the recorder. Set the "METER/MONITOR" switch to the "OUT X1" position and connect the recorder to the two red "OUTPUT" terminals. Adjust the "ZERO" control on the instrument so that the panel meter reads zero. Put the toggle switch on the rear of the instrument in the "1 mA" position, (up). Apply an input signal to the instrument and adjust its amplitude so that the panel meter reads full scale. (If the recorder does not deflect in the same direction as the panel meter, reverse its connections at the "OUTPUT" terminals). Rotate the "F. S. ADJUST" pot (also on the rear of the instrument) until the recorder also reads full scale.

CENTER ZERO OPERATION: In this mode of operation the recorder pen lies at the center of the chart when the panel meter reads zero[1], and at the (left) (right) edge of the chart when the panel meter reads (-) (+) full scale[1]. If such operation is desired, set the recorder pen to the center of the chart by means of the mechanical zero-adjuster on the recorder. Set the toggle switch on the rear of the instrument to the "1/2 mA" position (down) and proceed as outlined in the preceding section.

OPERATION WITH ESTERLINE-ANGUS RECTILINEAR RECORDER: This is a 1 milliamp full scale recorder with an internal resistance of about 6K. It may be used with the JB-5 but not quite as conveniently as the 1.5K type.

CENTER ZERO OPERATION: Follow the instructions for CENTER ZERO OPERATION above with the exception that the rear panel toggle switch must be placed in the "1 mA" (up) position.

1/With the "METER/MONITOR" switch in the "OUT X1" position.

LEFT-HAND ZERO OPERATION: Use the mechanical zero adjuster on the recorder to set the pen at the left hand edge of the chart at zero input. With the METER/MONITOR switch in the "OFF" or "OUT X1" position, plug the recorder into the two red MONITOR terminals. When the recorder reads full scale the JB-5 panel meter will read somewhat off-scale.[1] (This will not damage the panel meter).

There is no way to make the panel meter track the recorder under these conditions except by changing the meter multiplier resistors inside the Lock-In Amplifier.

RECORDER OPERATION, SERVO TYPE: Recorders of this type are voltage rather than current measuring devices. Thus it is necessary to convert the current output of the JB-5 into a voltage output whose range is suited to the recorder being used. These recorders are available either with single-ended (one input terminal grounded) or push-pull (floating) inputs. The latter are somewhat more compatible with the JB-5.

PUSH-PULL INPUT TYPES: When the JB-5 panel meter (in the "OUT X1" or "OUT X10" position of the METER/MONITOR switch) reads zero the two red OUTPUT terminals are at the same potential (-8V relative to chassis ground). A push-pull input recorder used with the JB-5 thus must be able to stand an 8V common-mode level at its input terminals (see recorder instruction manual). If this voltage (8V) exceeds the common-mode rating of the recorder it would be best to ground one of its input terminals and treat it as a single-ended recorder (see following section). If the recorder has this degree of common-mode rejection, proceed as follows: connect a resistor (preferably wirewound) across the input terminals of the recorder whose resistance R, in ohms is given by:

$$10^{-3} R = E_{F.S.}$$

where $E_{F.S.}$ is the voltage required at the input to the recorder to obtain a

fullscale deflection of the PEN. If the recorder is now connected to the OUTPUT terminals of the JB-5 the set-up instructions given in the preceding section for galvanometer-type recorders may be followed exactly. This will work with any recorder of this type having a full scale sensitivity of 2V. or less.

SINGLE-ENDED INPUT TYPES: In most cases it will be possible to simply "float" the recorder on the JB-5 output. To do this connect the ground terminal of the recorder to one of the JB-5 OUTPUT terminals, and the recorder input terminal to the other. THERE MUST BE NO CONNECTION BETWEEN THE RECORDER CHASSIS AND THE JB-5 CHASSIS. If the recorder has a 3 wire line cord an isolating adapter must be used. Connect a resistor whose value is obtained from the equation in the preceding section across the recorder input terminals and proceed as described in that section.

With some recorders using 60 cycle choppers, trouble may be encountered under these conditions due to 60 cycle pickup. Usually this can be cured by connecting a one microfarad or larger capacitor between the JB-5 chassis and the recorder chassis. (If an electrolytic capacitor is used, observe polarity; recorder chassis is at -8V relative to JB-5 chassis). If this fails to clear up the trouble (also try reversing recorder line plug in socket), then the procedure outlined below must be used.

Should "floating" operation not be possible then the following technique must be employed (See Figure 6).

Use lower red output terminal. In this case 1mA/1/2mA switch will not affect recorder operation.

The 10K potentiometer is adjusted until the JB-5 panel meter indicates zero when the recorder also indicates zero. (Left-hand, right-hand or center zero may be used for recorder). R is then adjusted so that when the JB-5 panel meter reads full scale, the recorder indicates full scale also. The value of R for center zero operation will be different (about 1/2) from that required

for left or right hand zero operation. It is recommended that mercury cells be used to establish the 8V "bucking potential".

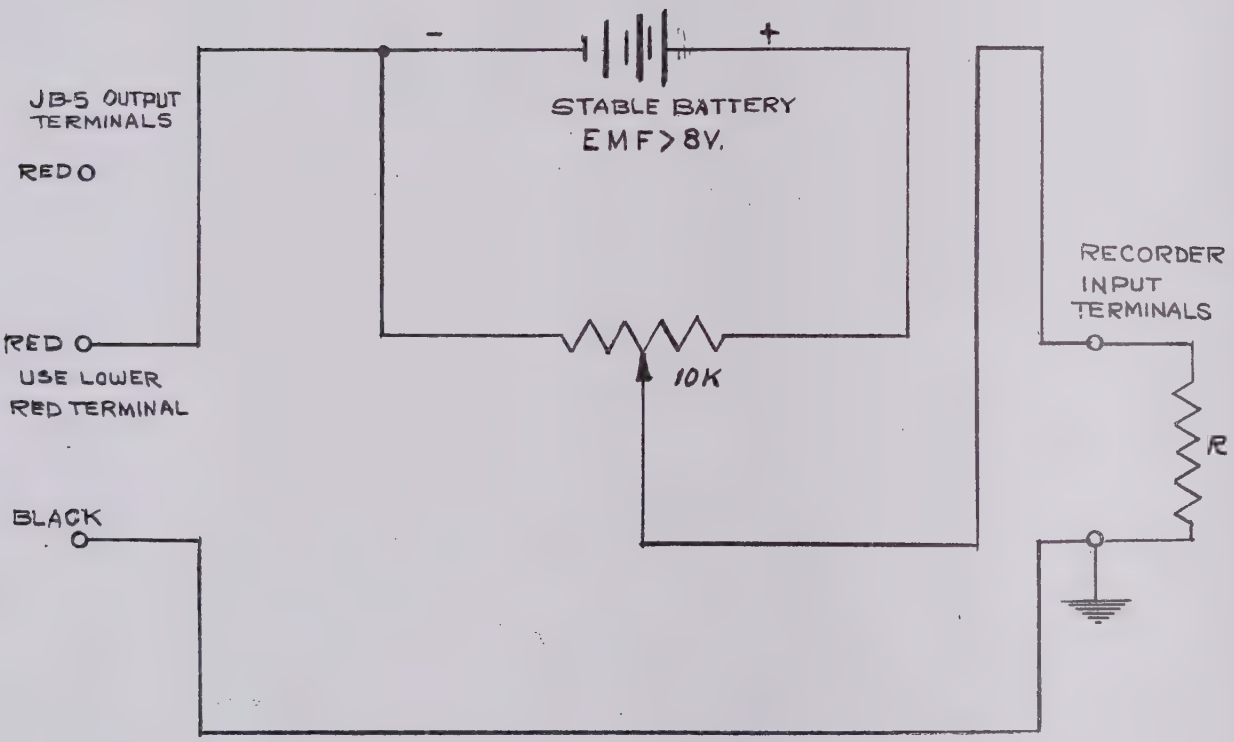


FIGURE 6

Some single-ended recorders have considerable "zero suppression" built in. This is usually expressed as a multiple of full scale. (eg. 10 X full scale). If sufficient zero suppression is available the somewhat simpler arrangement shown below can be used (Fig. 7).

for left or right hand zero operation. It is recommended that mercury cells be used to establish the 8V "bucking potential".

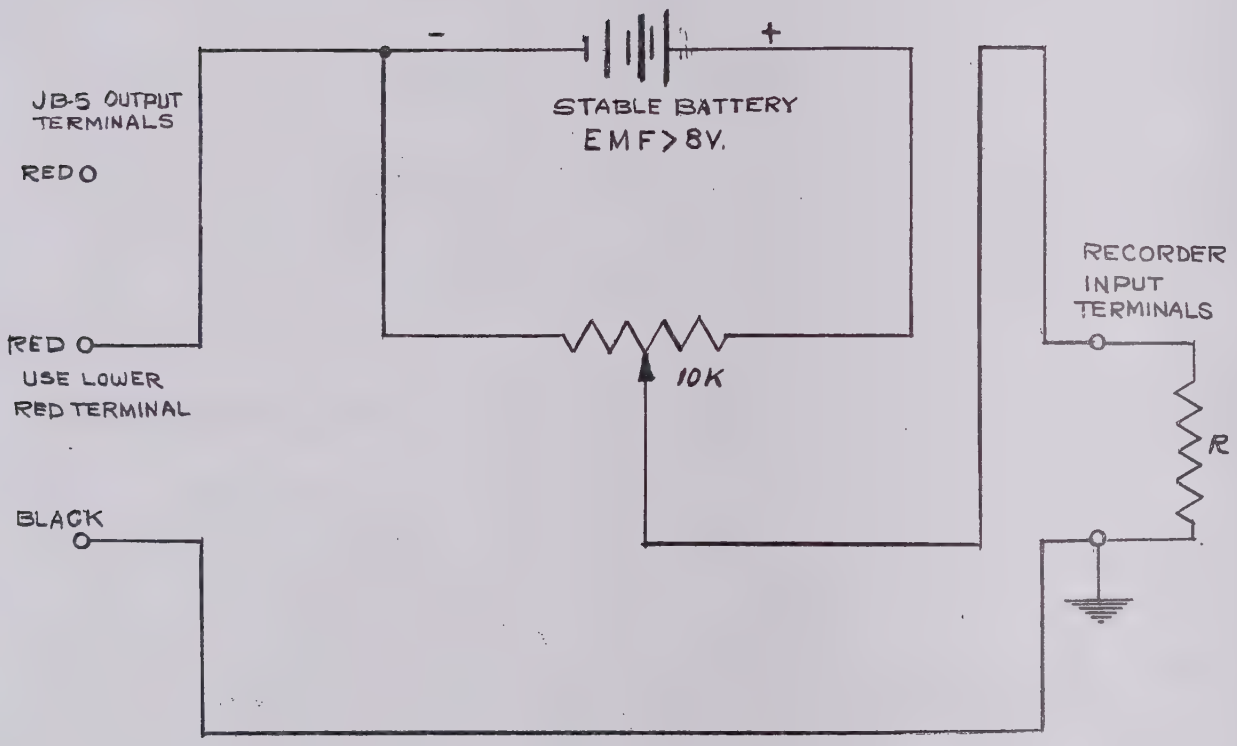


FIGURE 6

Some single-ended recorders have considerable "zero suppression" built in. This is usually expressed as a multiple of full scale. (eg. 10 X full scale). If sufficient zero suppression is available the somewhat simpler arrangement shown below can be used (Fig. 7).

$$8X\frac{R_2}{R_3 + 3K + R_1 + R_2} = 8\lambda = E_{Z.S.} \quad \text{where: } \lambda = \frac{R2}{R_3 + 3K + R_1 + R_2}$$

If full scale on the panel meter is to correspond to full scale on the recorder we must have: $2.5\ \lambda = E_{F.S.}$

$$\text{i.e. } \lambda = \frac{E_{F.S.}}{2.5}$$

$$\text{and hence: } \frac{8}{2.5} = \frac{E_{Z.S.}}{E_{F.S.}}$$

that is if the zero suppression built into the recorder is 8 ÷2.5 = 3.2 times full scale or better, it can be used in this way. R_3 which is the RECORDER ADJUST potentiometer located on the back of the JB-5 may be used for fine adjustment of full scale.

USE OF THE JB-5 TO DETECT SIGNALS AT HARMONICS OF SOME BASIC REFERENCE FREQUENCY

In some Lock-In Amplifier applications the signal appears at a harmonic of the basic reference frequency. In such a case the Lock-In Amplifier must be operated at this harmonic frequency. The JB-5 is ideally suited to this sort of operation due to its continuously tunable reference channel tuned amplifier. In general what one does is to feed the reference signal (assumed to be sinusoidal) into a simple harmonic generator and then pick off the desired harmonic by means of the tuned amplifier.

If even (2nd and higher) harmonics are of interest the following harmonic generator is useful:

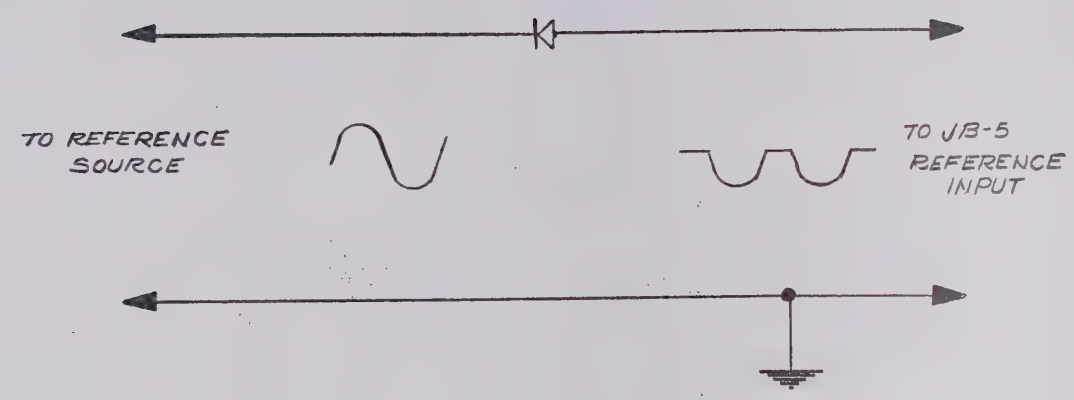


FIGURE 8

The amplitude of the reference sine wave should be several volts peak to obtain efficient rectification.

The Fourier Series of an ideal waveform of this type is:

$$E(t) = E_o \left[\frac{1}{\pi} + \frac{1}{2} \sin \omega t - \frac{2}{\pi} \sum_{k=2}^{\infty} \frac{\cos k\omega t}{k^2 - 1} \right] \quad (k \text{ even})$$

where E(t) is the output waveform of the half wave rectifier and E_o is the peak value of the input voltage. Apart from the fundamental, the output contains only even harmonics, the amplitude of the 2n th harmonic being:

$$A_{2n} = \frac{2}{\pi} E_o \frac{1}{4n^2 - 1}$$

and for the second harmonic n = 1 therefore $A_2 = \frac{2E_o}{3\pi}$

For operation in the SEL. EXT. mode a minimum level of 100 millivolts

peak to peak is required at the reference input terminals,

i.e. we must have

$$A_2 \geq 100 \text{ mV.}$$

i.e.
$$\frac{2E_o}{3\pi} \geq 100 \text{ mV.}$$

$$E_o \geq \frac{100 \times 3\pi}{2} \approx 500 \text{ mV.}$$

Since this is below the minimum value for efficient rectification by a silicon diode it is clear that any reference voltage large enough to produce the output wave form of Figure 8 will be adequate to drive the JB-5.

For second harmonic operation, simply feed the reference signal to the JB-5 via a diode as shown in Figure 8. Set the JB-5 FREQUENCY dial and FREQUENCY RANGE switch to the second harmonic of the reference frequency and proceed as described in the "SELECTIVE EXTERNAL" mode section of "OPERATING INSTRUCTIONS" in this manual.

Operation on even harmonics higher than the second is possible. The following considerations should be understood before such operation is attempted.

The reference channel tuned amplifier at full gain "1" can withstand an input of at least 1 V peak to peak and still maintain its normal selectivity characteristics. In order to assure proper operation the maximum instantaneous peak input voltage must be kept below this level. (Of course for settings of the REFERENCE LEVEL control other than "1" this limiting value is proportionately higher). If a high order harmonic is used the level of the harmonic of interest will be considerably lower than the maximum instantaneous peak input level (E_o). If we assign a maximum value of 1 to E_o then the peak value of the 2n th harmonic is:

$$A_{2n} = \frac{2}{\pi} \frac{1}{4n^2 - 1} \geq 50 \text{ mV}$$

This gives a maximum value for n of 2. i.e. This method will allow operation on even harmonics up to the 4th. For still higher even harmonics the following circuit can be used.

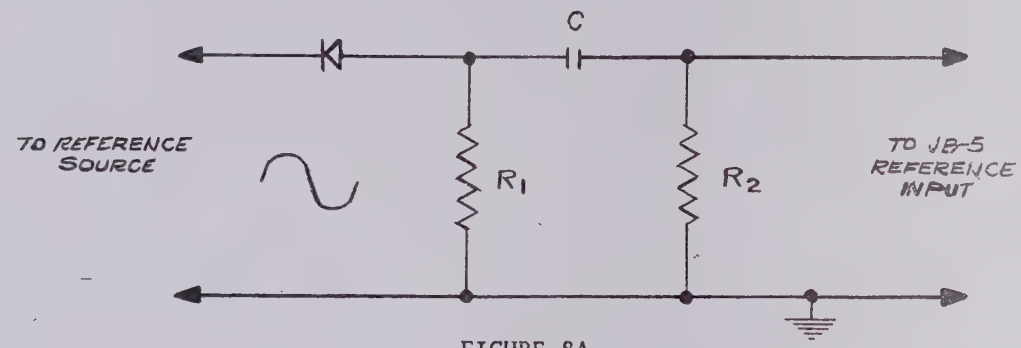


FIGURE 8A

R_2 and C constitute a high-pass filter whose effect is to reduce the level of the lower order harmonics. A good choice for R_2C is a value such that

$$\omega_n = \frac{1}{R_2C}$$

where ω_n is the angular frequency of the desired harmonic. Of course more elaborate preselection methods may be used.

In any case the JB-5 is set to the frequency of the desired harmonic and set up as outlined in the "SELECTIVE EXTERNAL" section of "OPERATING PROCEDURE" in this manual.

If odd (3rd and higher) harmonic operation is desired the following circuit can be used (Figure 9).

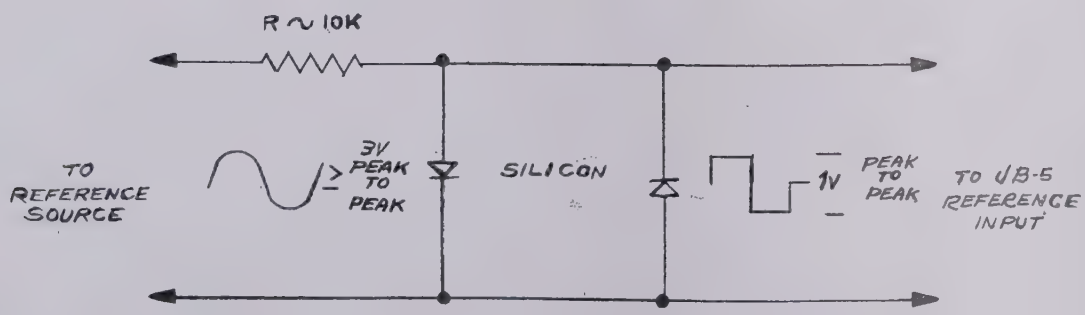


FIGURE 9

This gives a maximum value for n of 2. i.e. This method will allow operation on even harmonics up to the 4th. For still higher even harmonics the following circuit can be used.

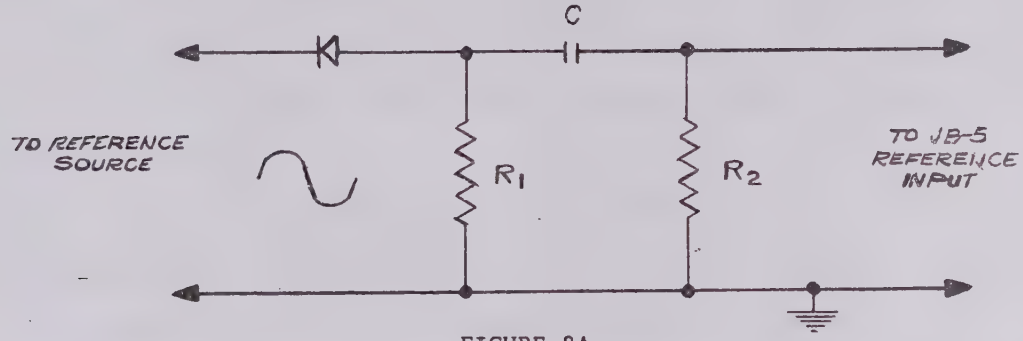


FIGURE 8A

R_2 and C constitute a high-pass filter whose effect is to reduce the level of the lower order harmonics. A good choice for R_2C is a value such that

$$\omega_n = \frac{1}{R_2C}$$

where $(\omega)_n$ is the angular frequency of the desired harmonic. Of course more elaborate preselection methods may be used.

In any case the JB-5 is set to the frequency of the desired harmonic and set up as outlined in the "SELECTIVE EXTERNAL" section of "OPERATING PROCEDURE" in this manual.

If odd (3rd and higher) harmonic operation is desired the following circuit can be used (Figure 9).

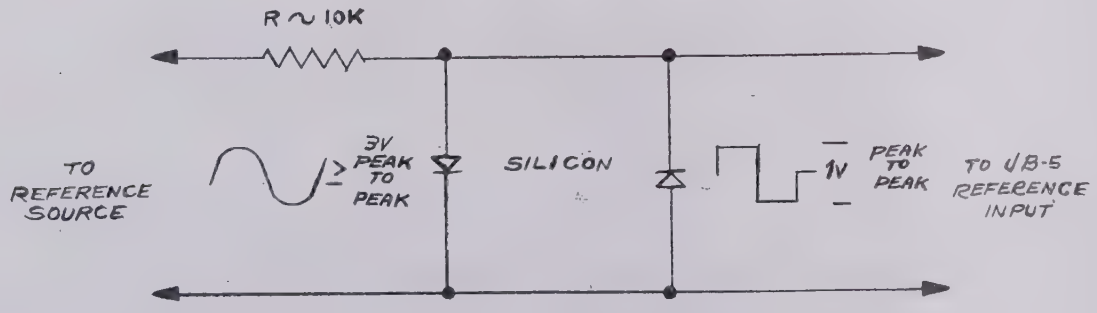


FIGURE 9

IV. CIRCUIT DESCRIPTION

INTRODUCTION: The various circuit elements that go together to make up this instrument are assembled on five plug-in printed circuit boards and one printed circuit board mounted on the front panel. The distribution is as follows: (the identification nos. referred to appear on the printed wiring side of the boards).

Plug in Board No. TA 0209A SIGNAL CHANNEL TUNED AMPLIFIER.

Plug in Board No. TA 0209B REFERENCE CHANNEL TUNED AMPLIFIER

Plug in Board No. PS 0112A PHASE SHIFTER AND A.C. VOLTMETER.

Plug in Board No. M 0113A MIXER AND D.C. AMPLIFIER.

Plug in Board No. P 0114 POWER SUPPLY.

Board No. OE 0311 "ODDS AND ENDS"

Access to these boards may be had by removing the back panel of the instrument. They are located as shown in FIG. 11 which is a back view with the rear cover removed.

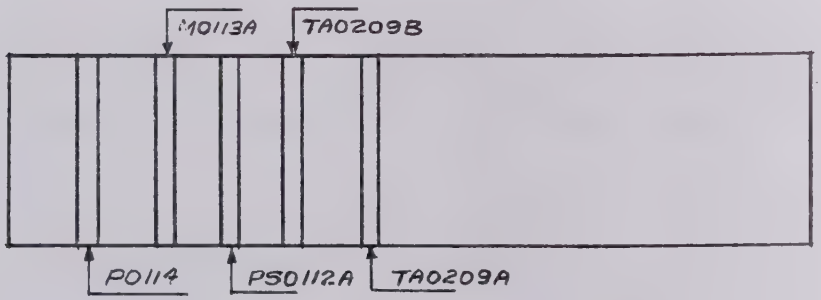


FIGURE 11

The circuitry will be described board by board in order to assist those with the task of troubleshooting the unit and those users who wish to

understand the principles of its operation.

PHASE SHIFTER (BOARD NO. PS 0112A): The continuously variable phase-shifter used in this instrument is based on the following circuit:

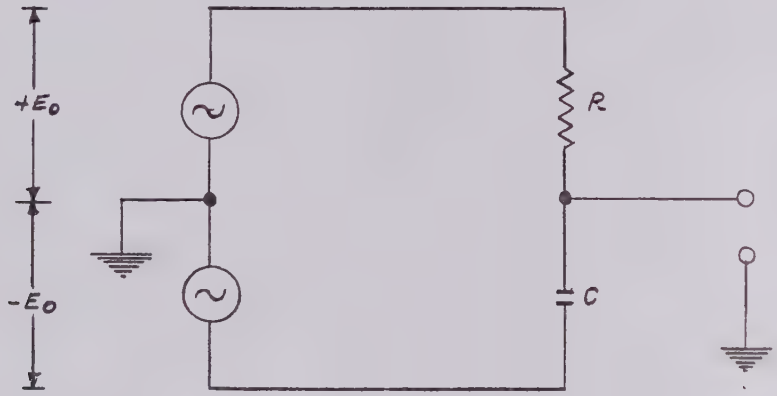


FIGURE 12

A series RC combination is driven from a push-pull voltage source whose output is 2 E_o volts. The output is taken between the RC junction and ground as shown. Upon analysis of this circuit one obtains the result that the output voltage is E_o, independent of R, C and the angular driving frequency (ω). The phase-angle, ϕ of the output voltage is given by:

$$\phi = 2 \text{ Tan}^{-1} \omega \text{ RC}$$

The phase-shifter employed in this instrument consists of a cascade of two such elements. The basic circuit of a single element is as shown in FIG. 13.

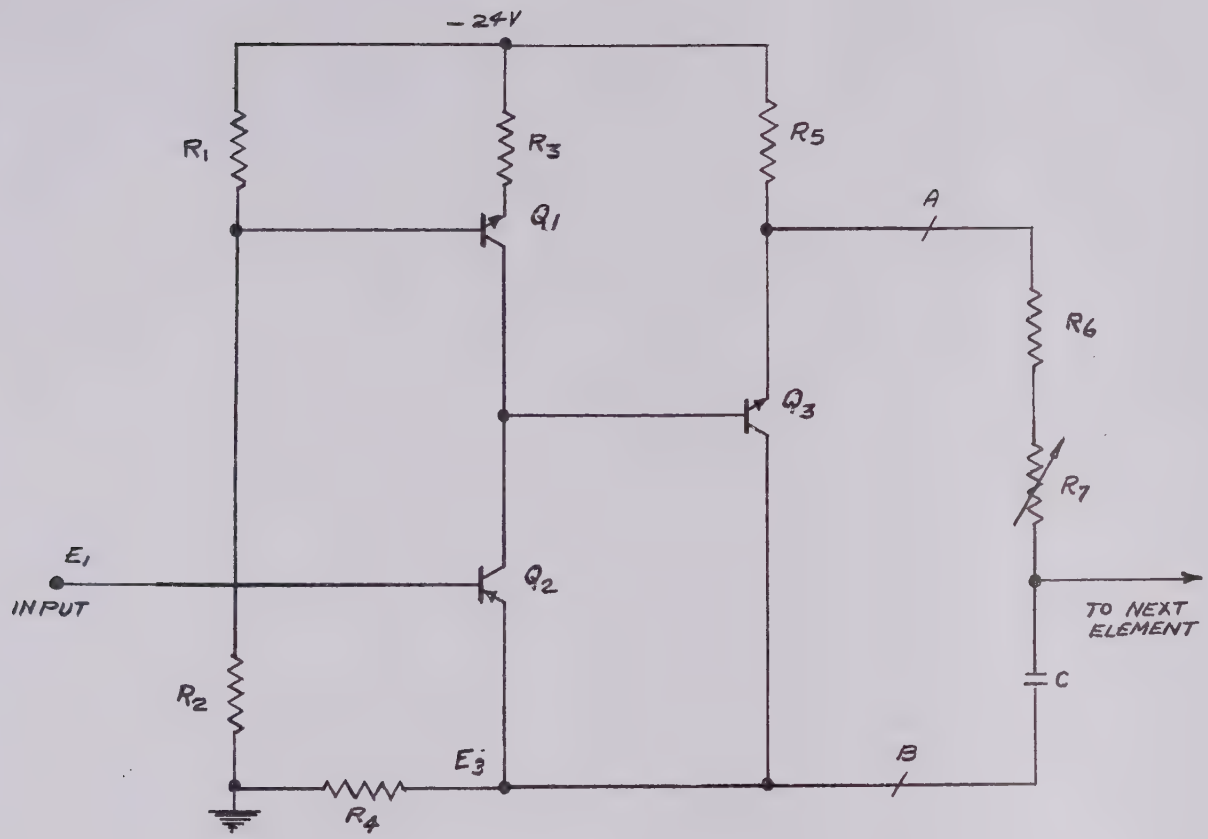


FIGURE 13

Q_1 is a current source for the collector of Q_2. This current is set by the voltage across R_3, which is determined by R_1 and R_2. Because the collector of Q_1 represents a very high impedance compared to the base of Q_3, essentially all of the signal current generated in the collector circuit of Q_2 due to an input voltage E_1 flows into the base of Q_3. The resulting signal current in the emitter of Q_3 flows into R_5, that in the collector flows into R_4, (assuming for the moment that the load is disconnected at points A and B). If i_{c_3} is the signal collector current, and i_{e_3} the signal emitter current in Q_3, then very nearly,

$$i_{c_3} = \alpha_3 \; i_{e_3}$$

where α_3 is the grounded base forward current transfer ratio of Q_3 and thus i_{R_5} the signal current in R_5 is given by:

$$i_{R_5} = \frac{1}{\alpha_3} \; i_{c_3}$$

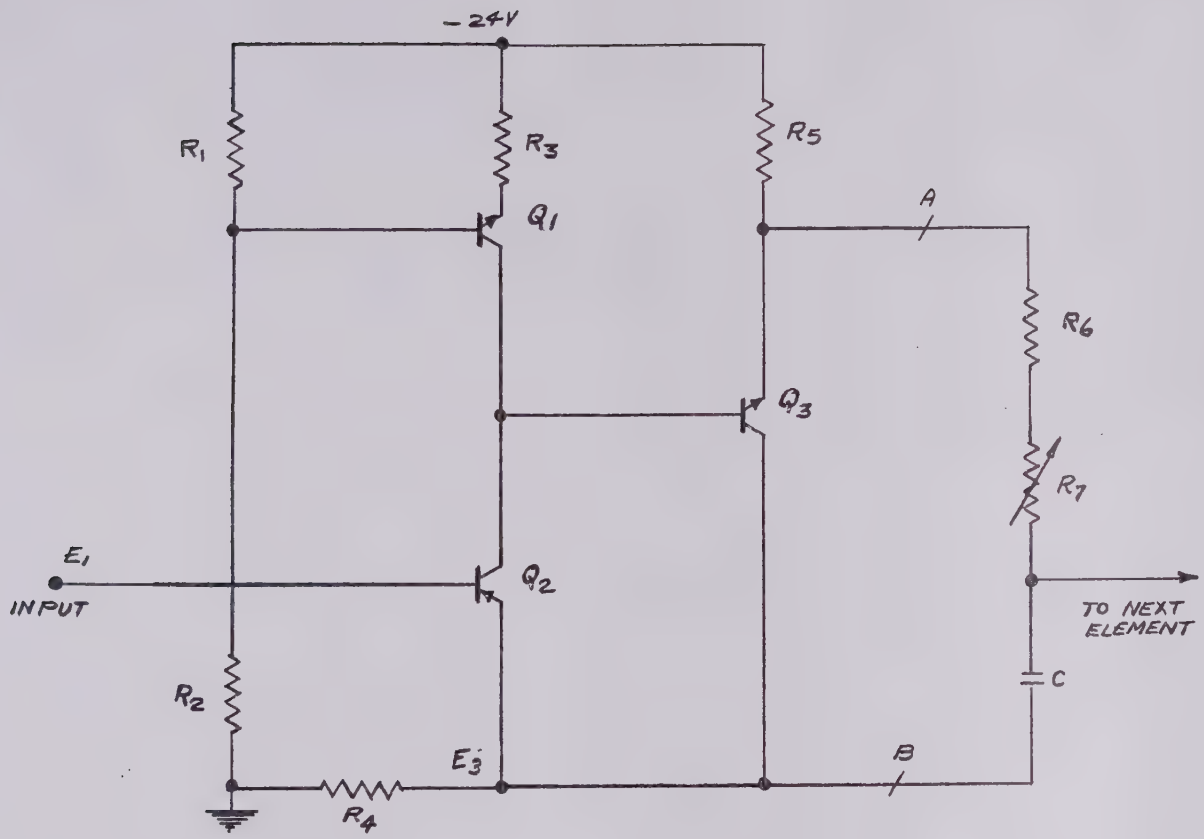


FIGURE 13

Q_1 is a current source for the collector of Q_2. This current is set by the voltage across R_3, which is determined by R_1 and R_2. Because the collector of Q_1 represents a very high impedance compared to the base of Q_3, essentially all of the signal current generated in the collector circuit of Q_2 due to an input voltage E_1 flows into the base of Q_3. The resulting signal current in the emitter of Q_3 flows into R_5, that in the collector flows into R_4, (assuming for the moment that the load is disconnected at points A and B). If i_{c_3} is the signal collector current, and i_{e_3} the signal emitter current in Q_3, then very nearly,

$$i_{c_3} = \alpha_3 \; i_{e_3}$$

where α_3 is the grounded base forward current transfer ratio of Q_3 and thus i_{R_5} the signal current in R_5 is given by:

$$i_{R_5} = \frac{1}{\alpha_3} \; i_{c_3}$$

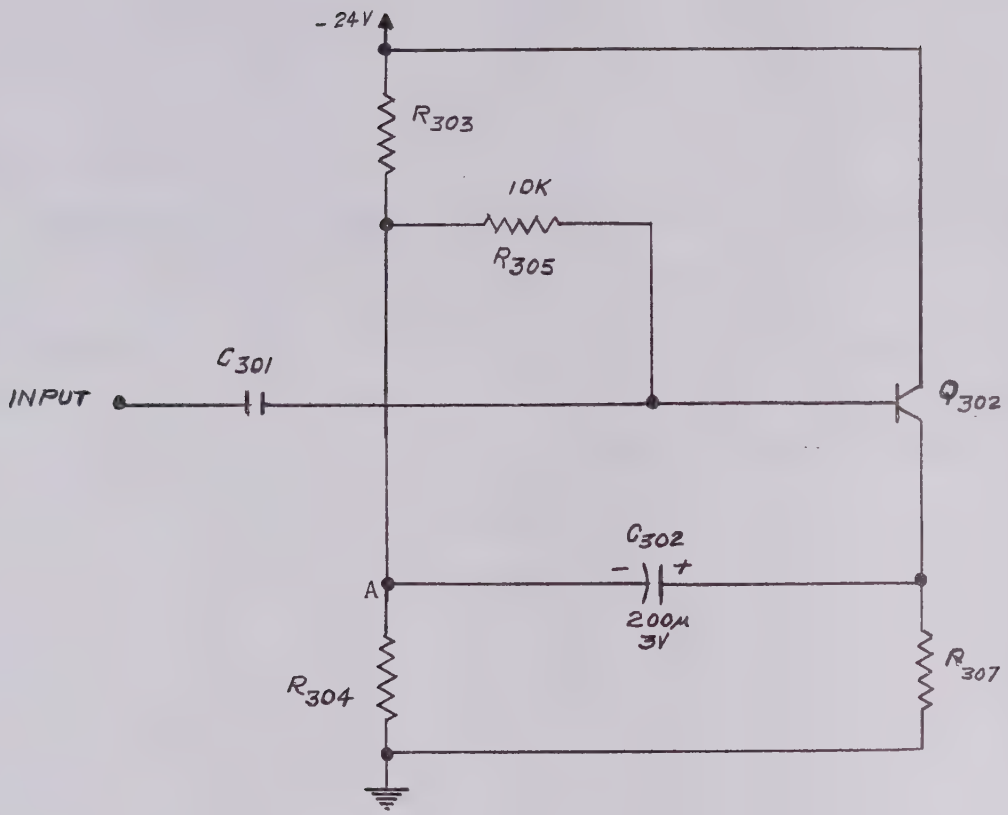


FIGURE 14

C_{302} (see FIG. 14 and schematic diagram 0400-C-SD) has the effect of making the signal voltage at the emitter of Q_{302} appear also at point A. If e_b is the signal voltage on the base of Q_{302} the voltage thus impressed on point A will be Ke_b where K is typically $\approx$ 0.98. Thus the voltage across R_{305} will be:

$$E_R = e_b - Ke_b \approx 0.02\, e_b$$

i.e. the signal current flowing into R_{305} will be about 1/50 of what it would be without C_{305}. Thus it gives R_{305} an effective value $\approx$ 500K and greatly increases the A.C. impedance at the base of Q_{302}. This circuit is used elsewhere in the instrument to increase input impedance.

The component values in the phase-shifter elements themselves are chosen so that more than 180° of continuously variable phase shift is provided at any frequency within the range of the instrument. Thus, by making use of the "INVERT" switch any desired phase angle may be set between the signal and

reference channels.

A.C. VOLTMETER/AMPLIFIER (BOARD NO. PS 0112): The input of the peak to peak voltmeter circuit is point V2 (see schematic diagram #0400-C-SD), its output being point W1. If a sine wave of E volts peak to peak swinging about any D.C. reference level is applied to V2, C_{305} will, due to the action of D_{301}, charge until the base voltage of Q_{312} swings as shown in FIGURE 15.

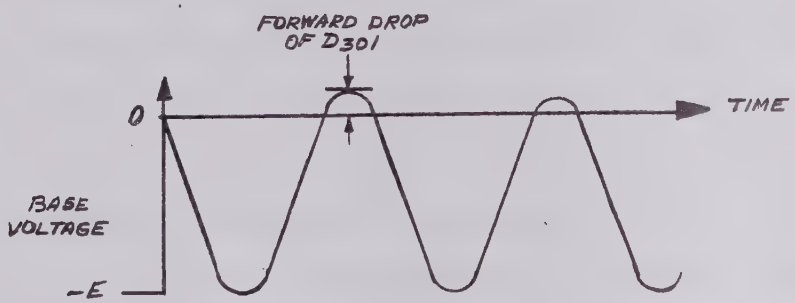


FIGURE 15

The voltage at the junction of R_{328}, and R_{329} will be as shown in FIG. 16.

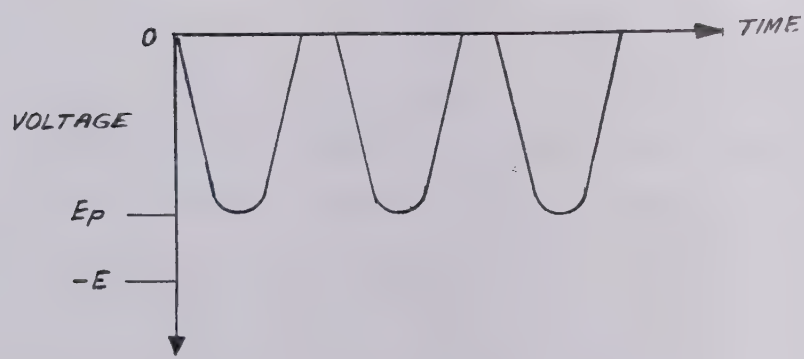


FIGURE 16

E_p will be given by:

$$E_p = E - (\text{forward drop } D_{301} + \text{base emitter drop } Q_{312}).$$

The D.C. average of this waveform will appear across C_{306}. This D.C. voltage is thus a function of the peak to peak voltage of the input waveform. This voltage is indicated by the panel meter in the "SIG." and "REF". positions of the "METER/MONITOR" switch.

In the "REF." position of the "METER/MONITOR" switch (see chassis wiring diagram #0398-C-SD) point V2 is connected to phase-shifter output point P_2. The A.C. voltage normally appearing at this point is about 5V. peak to peak. This voltage applied to V2 will produce, at W1, a D.C. voltage of about 1.5V as indicated by a meter with an impedance large compared to 22K.

In the "SIG." position of the "METER/MONITOR" switch (see chassis wiring diagram #0398-C-SD) point V2 is connected to the output of the meter amplifier (W2). The input to this amplifier is then connected to output point O_2 (see signal channel tuned amplifier schematic diagram #0403-C-SD) of the signal channel tuned amplifier. Since the nominal level at this point is of the order of 60 M.V. peak to peak the amplifier is needed to bring the level of the input to the voltmeter circuit up to $\approx$ 5V. peak to peak. This amplifier uses the same circuit as that shown in FIG. 13 and the description accompanying FIG. 13 describes the operation of this amplifier as well. In this case however the ratio of the emitter resistors (R_{324}/R_{323}) is 100 instead of unity, and as a result this amplifier has (from V1 to W2) a gain of 100.

TUNED AMPLIFIER (BOARD NOS. TA0209A AND TA0209B): (See schematic diagrams #0403-C-SD and 0404-C-SD). If one studies the aforementioned diagrams it will be evident that these two (reference channel and signal channel) tuned amplifiers are identical except for a few specific component values, and their nominal

operating voltage. The different operating voltages are due to extra filtering and regulation being used on the voltage supply to the signal channel in order to reduce ripple in this supply voltage and to eliminate possible coupling to the reference channel via the power supply. Consequently this description will be made in terms of the reference channel unit, with the understanding that it applies to both.

The tuned amplifiers are based on the resonant transfer function of the following circuit:

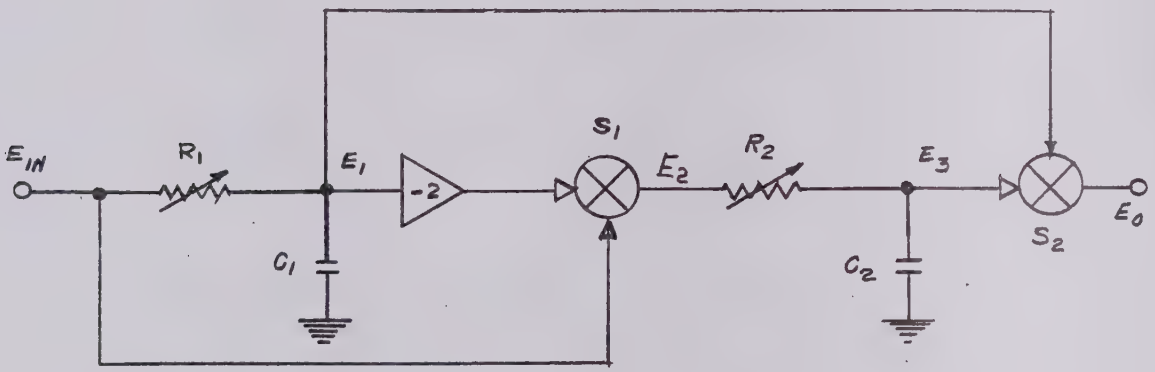


FIGURE 17

$$\frac{E_1}{E_{in}} = \frac{1}{1 + j\omega R_1 C_1}$$

$$\frac{E_2}{E_{in}} = \frac{E_{in} - 2E_1}{E_{in}} = 1 - \frac{2}{1 + j\omega R_1 C_1} = \frac{-1 + j\omega R_1 C_1}{1 + j\omega R_1 C_1}$$

$$\frac{E_3}{E_{in}} = \frac{E_2}{E_{in}} \times \frac{1}{1 + j\omega R_2 C_2} = \frac{-1 + j\omega R_1 C_1}{(1 + j\omega R_1 C_1)(1 + j\omega R_2 C_2)}$$

$$\frac{E_o}{E_{in}} = \frac{E_3 + E_1}{E_{in}} = \frac{-1 + j\omega R_1 C_1}{(1+j\omega R_1 C_1)(1+j\omega R_2 C_2)} + \frac{1 + j\omega R_2 C_2}{(1 + j\omega R_1 C_1)(1 + j\omega R_2 C_2)}$$

$$= \frac{j\omega(R_1C_1 + R_2C_2)}{1 - \omega^2 R_1C_1R_2C_2 + j\omega(R_1C_1 + R_2C_2)} \quad \ldots\ldots 1$$

The normalized current in a series RLC resonant circuit is given by:

$$\frac{i}{i_o} = \frac{j\frac{\omega}{Q_o\omega_o}}{1 - (\frac{\omega}{\omega_o})^2 + j\frac{\omega}{\omega_o Q_o}}$$

where $\omega_o = \frac{1}{\sqrt{LC}}$ $\quad Q_o = \frac{\omega_o L}{R}$ $\quad i_o = \frac{E}{R}$

Equation 1 is of the same form as the equation for $\frac{i}{i_o}$ where:

$$\omega_o = \frac{1}{\sqrt{R_1C_1R_2C_2}} \quad \text{and} \quad Q_o = \frac{\sqrt{R_1C_1R_2C_2}}{R_1C_1 + R_2C_2}$$

If we define $R = \sqrt{R_1R_2}$ $\quad C = \sqrt{C_1C_2}$ $\quad$ and let $R_1 = aR$ $\quad C_1 = bC$

$R_2 = R/a$ $\quad C_2 = C/b$

Then $\omega_o = \frac{1}{RC}$; $\quad Q_o = \frac{1}{ab + \frac{1}{ab}} = \frac{ab}{1 + (ab)^2}$

Note, however, that the transfer function is unity at resonance, independent of the values of a and b. In practice, the capacitors have nominally the same value with a tolerance of 5%, so b lies within $1 \pm .05$; tolerances (including tracking) on the R's are similar. The effect on Q_o is completely negligible: if $ab = 1 + \delta$, then

$$Q_o \approx \frac{1}{2}\left[1 - \frac{\delta^2}{2}\right]$$

and for $\delta \approx .1$, the Q_o differs from the maximum value by only 1/2%.

The actual circuit follows the block diagram of FIG. 17 closely except for a phase inversion right at the input and a liberal use of current sources

in place of voltage sources (See Schematic Diagram 0404-C-SD). Let us take the base of Q_{203} as our input point. Any voltage E_{in} appearing there is transmitted to the base of Q_{205}, which then is a source of current

$$i_1 = \frac{-E_{in}}{R_{213}}$$

This current flowing in R_{214} produces a voltage

$$i_1 R_{214} = -E_{in} \frac{R_{214}}{R_{213}} \approx -E_{in}$$

with a source impedance equal to R_{214}; this resistor is thus a part of the frequency determining RC network. The voltage appearing at the base of Q_{209} is then the E_1 of the block diagram except for a sign change. This voltage is also applied (through the emitter followers Q_{209} and Q_{211}) to the base of Q_{204}, which provides the gain of -2. It also acts as the (current) summing junction S_1, accepting a current $-E_{in}/R_{210}$ from Q_{203}. The summed currents flowing in R_{215} produce (-) E_2, and the base of Q_{206} receives (-) E_3.

The voltages which must finally be summed thus appear at the bases of Q_{206} and Q_{209}. To keep loading to a minimum, the impedances at these points must be as high as possible. This is accomplished by means of the additional current gain provided by Q_{207} and Q_{208}; this sum current is equivalent to the output in the block diagram of FIG. 17.

Finally, the Q must be raised from 1/2 to the desired value of 25. This is accomplished by means of the following positive feedback network:

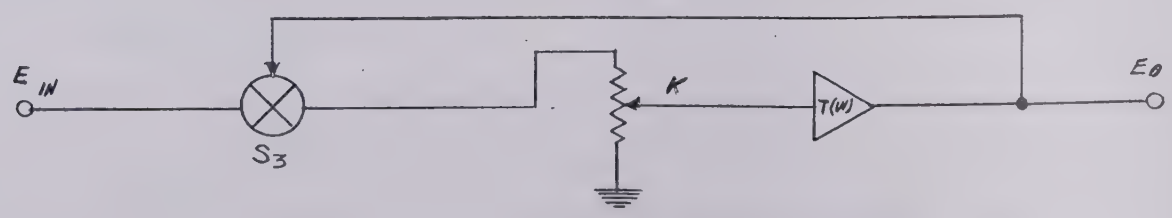


FIGURE 18

The transfer function $T(\omega)$ is the one generated above:

$$T(\omega) = \frac{j \frac{\omega}{Q_o \omega_o}}{1 - (\frac{\omega}{\omega_o})^2 + j \frac{\omega}{Q_o \omega_o}}$$

Solving the loop equation, we find

$$K(\omega) = \frac{eo}{e_{in}} = \frac{k\, T(\omega)}{1 - k\, T(\omega)} = \frac{k\, j \frac{\omega}{Q_o \omega_o}}{1 - (\frac{\omega}{\omega_o})^2 + j \frac{\omega}{Q_o \omega_o} - k\, j \frac{\omega}{Q_o \omega_o}}$$

$$= \frac{k}{1-k} \quad \frac{j \frac{\omega}{\omega_o} \quad \frac{1-k}{Q_o}}{1 - (\frac{\omega}{\omega_o})^2 + j (\frac{\omega}{\omega_o}) \frac{1-k}{Q_o}}$$

Thus $|K(\omega)|_{max} = k/(1-k)$; $Qeff = \frac{Q_o}{1-k}$

To make Qeff = 25, we need $1 - k = \frac{1}{50}$, or $k = .98$ and $K = 49 \approx 2\ Qeff$

In the actual circuit, the input transistor, Q_{202}, operates in the same basic way as the input transistor in the phase shifter. The output current of this transistor is added to the output current of the frequency selective network at the emitter of Q_{201}, which thus serves as the summing junction S_3 of FIG. 18. The Q adjustment is then provided in R_{206}, which sets the k of FIG. 18.

Since the output of the frequency selective network does not appear as a voltage until it has already been added to the input at R_{206}, it is necessary to duplicate the summing junction S_2 (FIG. 17) for the final output. This is done by means of Q_{210} and Q_{211}, whose currents are summed at Q_{212} and sent to R_{223}. This signal then drives the split-load phase inverter Q_{213} which provides the push-pull output for the mixer.

If the behavior of the tuned amplifier is to duplicate the behavior of an RLC tuned circuit exactly it is necessary that the output of the tuned amplifier go to zero at frequencies far above and far below the resonant frequency. From FIG. 17 it is clear that since both E_1 and E_3 approach zero at high frequencies

due to the action of C_1 and C_2, and since $E_o = E_1 + E_3$ there will be no output at these frequencies. At very low frequencies C_1 and C_2 may be neglected and we have

$$E_1 = E_{in}$$

$$E_2 = E_{in} - 2E_{in} = -E_{in}$$

$$\text{and} \quad E_3 = E_2 = E_{in}$$

$$\therefore \quad E_1 = -E_3 = 0$$

and once again there is no output. This last case depends on the gain of the amplifier preceding S_1 being exactly -2. R_{211}, the "LOW FREQUENCY BALANCE" adjustment enables this gain to be so adjusted.

MIXER, D.C. AMPLIFIER (BOARD NO. M0113): Refer to schematic diagram (#0399-C-SD). The mixer proper consists of Q_{401} through Q_{406} and their associated components. The points marked α and β are driven 180° out of phase by a square wave at the reference frequency. They swing from approximately -9V to -7.5V relative to ground. Q_{403} and Q_{406} function as current sources, their collector currents being proportional to the instantaneous signal levels at M_1 and M_2. Assume for the moment that no signal is applied to M_1 and M_2. In this case the D.C. collector currents of Q_{403} and Q_{406} are determined by the bias string (R_{401}, R_{404}) and their respective emitter resistors. Consider what happens during the half-cycle when point α is at -9V and β is at -7.5V. The emitters of Q_{401} and Q_{405} will be held at about -8.7V, along with the emitter of Q_{402} and Q_{404}. Since the bases of Q_{402} and Q_{404} are thus at +1.2V relative to their emitters these transistors will be cut off. As a result, the collector current of Q_{403} will flow into R_{405} and that of Q_{406} will flow through R_{411}, the voltage drop across these resistors being determined by their respective currents. On the next half cycle the situation is reversed, Q_{403} feeding R_{411},

Q_{406} feeding R_{405}. In general the quiescent D.C. collector currents of Q_{403} and Q_{406} will not be the same and as a result the voltage across R_{405} and R_{411} will change each half cycle. The result will be a reference frequency square wave at these two points. If R_{409} is adjusted to equalize these currents the square wave will disappear. If an A.C. input signal is now applied between M_1 and M_2 the resulting A.C. collector currents in Q_{403} and Q_{406} will be switched between R_{404} and R_{411} in just the way described above. Thus the mixer functions as a double-pole throw switch, being driven at the reference frequency. If a sine wave at the reference frequency and in phase with it is applied to the mixer input the output waveform will appear as shown in FIG. 19.

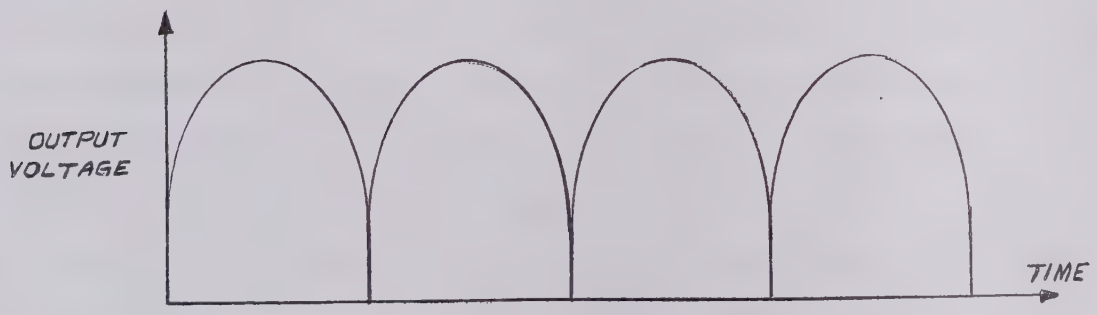


FIGURE 19

The mixer is followed by a conventional balanced D.C. amplifier consisting of Q_{407} through Q_{413} and their associated components. (Components enclosed in dashed lines are not on the board; see chassis wiring diagram #0399-C-SD).

OVERLOAD INDICATOR:

The utility of a coherent detector of this type in recovering a signal from noise is due to the fact that of the total spectrum (signal plus noise) applied to its input only the signal plus whatever of the noise lies within a bandwidth determined by the output filtering about the signal frequency contributes to the detector output. In order for this to be true it is necessary that the output of the mixer be a linear function of its input. If this is not the case, noise lying outside the bandwidth interval discussed above will contribute to the mixer output through cross-modulation ("noise beating with noise") and other effects. The result being to degrade the signal to noise at the mixer output. Since non-linearity will result from mixer overload it is important to prevent this from occuring.

In the JB-5, an overloaded condition at the mixer output will be indicated by an off-scale reading of the panel meter in the "OUT X1" position of the "METER MONITOR" switch. If this were the only possible mixer overload condition, overload prevention would consist merely of monitoring the panel meter. However in an application where the signal to noise ratio at the mixer input is poor, with a long time constant being used, it is possible that the mixer is being over-driven even though the output signal is quite small. The reason being that under these conditions (poor signal to noise) the part of the mixer input spectrum which determines the mixer output (signal plus noise close to signal frequency) contributes only a small fraction to the level at the mixer input. Thus the level (signal plus noise) at the mixer input can be sufficient to overload it even though the mixer output is small. It is an overload condition of this type that is indicated by the "OVERLOAD" light.

The operation of the "OVERLOAD" light is based on the behavior of the point marked O.V. (wiper of R409, see schematic diagram #0399-C-SD). The mixer operates by switching the collector currents of Q_{403} and Q_{406} alternately to R_{405} and R_{411}. The mixer will be linear as long as the collector currents of

these transistors are linear functions of their base voltages. The points M_1 and M_2 are driven by the push-pull output of the signal channel tuned amplifier. Thus the voltage swings on the bases of Q_{403} and Q_{406} are equal and opposite. (assuming the push-pull output of the tuned amplifier to be perfectly balanced). Under these conditions when the emitter current of one of Q_{406}, Q_{407} increases the other will decrease by just the same amount. The result is that the current in R_{410} remains constant at its quiescent value of about 2 m.a.. When the base of either Q_{405} or Q_{406} has risen to about 1.5V above its quiescent level its emitter current will have increased 2 m.a. above its quiescent value. At this point the other transistor will be cut off. This represents the absolute limit of the linear range of the mixer. If the current of the conducting transistor increases beyond this point the current through R_{410} will change since there can now be no compensating decrease in the current of the other (cut-off) transistor.

As a result, when an overload of this kind occurs a voltage waveform like that shown in FIGURE 20 appears at "0.V".

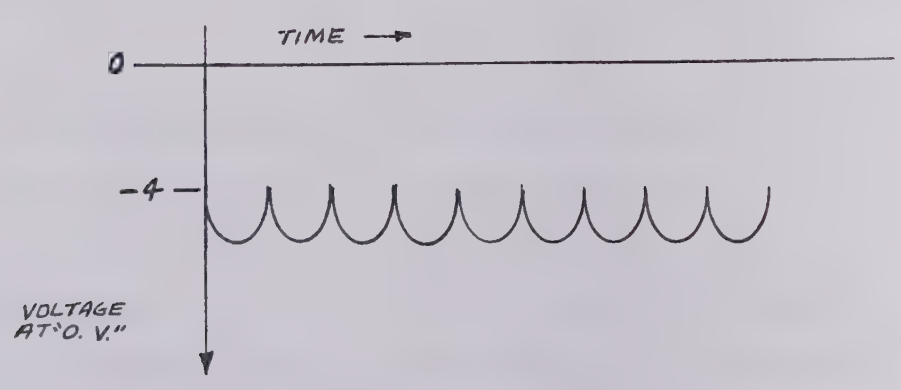


FIGURE 20

After amplification this waveform is applied to the base Q_2. When such an over-

load occurs, Q2 begins to conduct, thus causing the overload light, B3, to glow. (See FIG. 21)

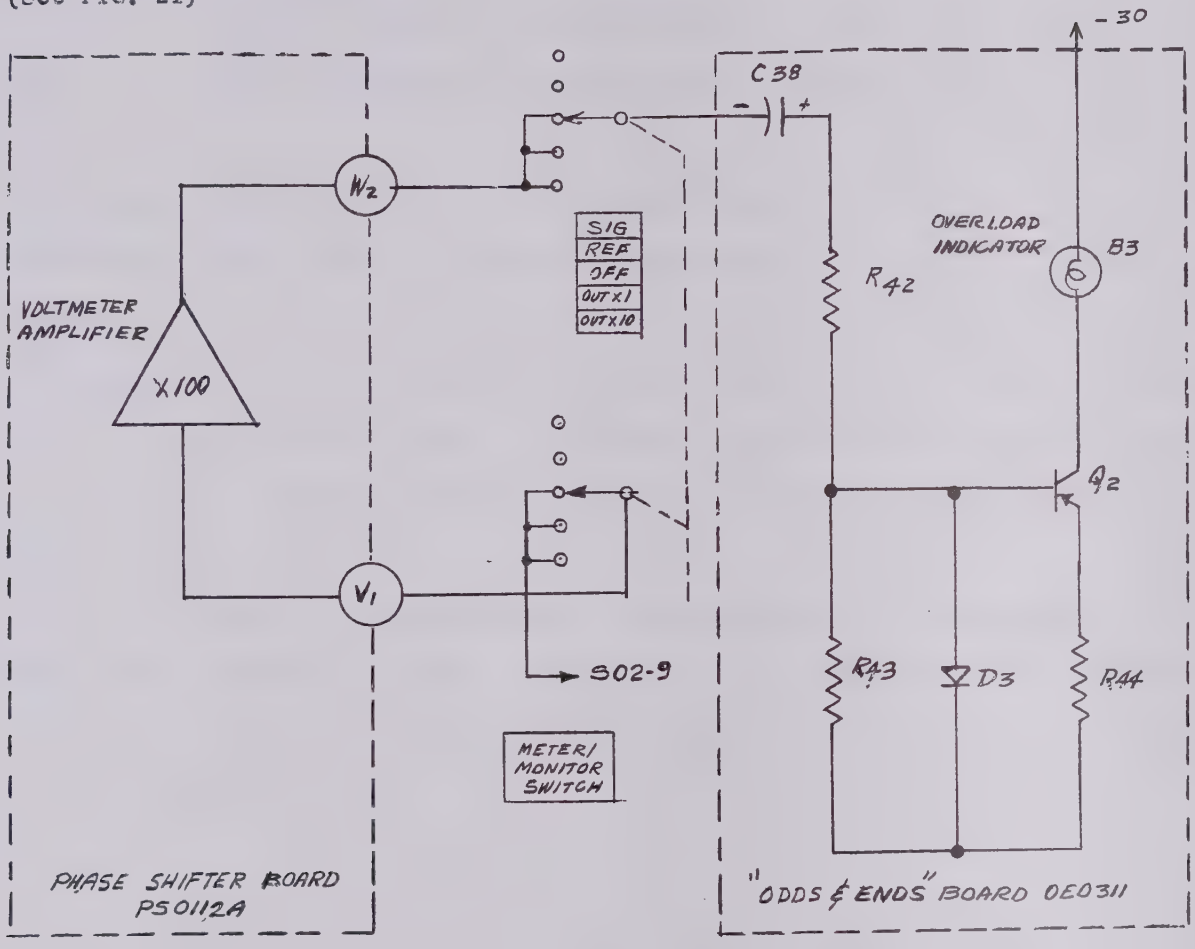


FIGURE 21

In the discussion above it was assumed that the signals applied to M_1 and M_2 were perfectly balanced. In practice this will not be the case, and the result will be that a small signal will appear at O.V. even when the mixer is operating linearly. The effect of this is to make the "OVERLOAD" indicator glow a bit prematurely. This is a "good fault" in the sense that, as a result, one may rest assured that as long as the "OVERLOAD" indicator does not glow, that the mixer is operating linearly.

Q_{414} through Q_{416} and their associated components make up a Schmitt trigger circuit which generates the square wave to drive the mixer. Q_{417} is a driver

for the Schmitt trigger circuit.

This circuitry is completely conventional and no trouble should be encountered in trouble-shooting it by anyone versed in conventional transistor circuitry.

POWER SUPPLY (BOARD NO. P0114): This board contains the -24V and -20V regulators. The circuits are completely conventional and require no detailed discussion. The output current (about 250 mA. in normal operation) is limited by D_{508} and R_{503} to about 500 mA under short cicuit. If either supply is shorted, F_{501} will blow in about 1 minute. If trouble is encountered with the unit and F_{501} is found to be blown, locate the short circuit that caused it, correct it, and replace F_{501}.

NOTE: UNDER NO CIRCUMSTANCES USE A FUSE OTHER THAN THE TYPE SPECIFIED ON THE SCHEMATIC DIAGRAM AS A REPLACEMENT FOR F_{501}. IF AN EXCESSIVELY HIGH CURRENT FUSE WERE INSTALLED HERE A PROLONGED SHORT CIRCUIT COULD RESULT IN THE DESTRUCTION OF Q_{501}, Q_{508} OR BOTH.

V. ALIGNMENT PROCEDURE

INTRODUCTION: Removing the back cover from the instrument by removing the 6 screws that hold it in place gives access to all the alignment adjustment points except the oscillator level adjustment. The top panel must be removed for this adjustment.

SIGNAL CHANNEL TUNED AMPLIFIER: This board is on the extreme right viewed from the back of the instrument. It has two alignment Trimpots: "Q-ADJUST" being the lower one (R_{106}) and "LOW FREQUENCY BALANCE" the upper (R_{111}). In setting these up, the "LOW FREQUENCY BALANCE" should be set first as this adment affects the Q. The Q adjustment has very little effect on the "LOW FREQUENCY BALANCE".

LOW FREQUENCY BALANCE: This adjustment minimizes the response of the tuned amplifier to frequencies far from the resonant frequency. To adjust it, set the "METER/MONITOR" switch to "SIG.", the "SIGNAL LEVEL" switch to "1", and the "FREQUENCY" and "FREQ. RANGE" controls to give 150Kc. Apply a signal of about 1 volt peak to peak at 15 to 60 cps to the signal input terminals. Adjust the "LOW FREQUENCY BALANCE" trimpot until the output seen at the monitor terminals by detector of millivolt sensitivity is a minimum.

Q ADJUST: This adjustment sets the Q of the tuned amplifier. To adjust it, set the "METER/MONITOR" switch to "SIG.", the "SIGNAL LEVEL" control to ".02", the "FREQUENCY" and "FREQUENCY RANGE" controls to a frequency of 400 cycles and apply a 400 cycle sine wave of about 50 mV. peak to peak to the signal input terminals. Connect a scope or A.C. voltmeter between either of the two

red monitor terminals and ground and adjust either the frequency of the applied signal or the "FREQUENCY" dial on the lock-in amplifier for maximum output. This having been done, vary the "Q ADJUST" control until the signal level at the "MONITOR" terminals is the same as that applied to the "SIGNAL INPUT" terminals. This sets the on-resonance gain of the tuned amplifier at 50 and since this gain is just 2Q it sets the Q at 25. The Q of both tuned amplifiers varies somewhat with frequency.* A Q of 25 may be obtained at any frequency in the range by making the adjustment described above at that frequency. If a Q value different than 25 is desired, it may be obtained by setting the gain of the tuned-amplifier under these conditions to twice the desired Q value.

REFERENCE CHANNEL TUNED AMPLIFIER: This board is located second from the right as viewed from the back of the instrument. It has the same two adjustments as the signal channel tuned amplifier board, located in the same relative positions, and the adjustment procedure is the same as for the signal channel tuned amplifier board except for the differences noted below:

LOW FREQUENCY BALANCE: This adjustment is made in the same way as outlined for the signal channel except that the "METER/MONITOR" switch is set to the "REF." position, "REFERENCE MODE" switch to the "SEL. EXT." position and the input signal is applied to the "REFERENCE IN/OUT" terminals. The "MONITOR" terminals in this case are single-ended; the lower red terminal is at chassis ground.

Q ADJUST: This adjustment is made in the same way as outlined for the signal channel except that the "METER/MONITOR" switch is placed in the "REF." position, the "REFERENCE MODE" switch in the "SEL. EXT." position, "REFERENCE LEVEL" control at ".02", and the input signal is applied to the "REFERENCE IN/OUT" terminals. The level of the input signal can be as much as 1V peak to

* See Page 3

peak. "MONITOR" terminals in this case are single-ended; the lower red terminal is at chassis ground.

PHASE SHIFTER A.C. VOLTMETER BOARD: This board is third from the right as viewed from the back of the instrument. There are no alignment adjustments on this board.

MIXER D.C. AMPLIFIER BOARD: This board is located second from the left as viewed from the back of the instrument. There are three adjustment trimpots on this board. The upper most one is the "SYMMETRY" adjustment (R_{437}), the middle one the "HYSTERESIS" adjustment (R_{427}) and the lower one the "A.C. BALANCE" adjustment (R_{409}). These should be adjusted in the order given below:

HYSTERESIS CONTROL: Turn the "SIGNAL LEVEL" control to "OFF", the "REFERENCE MODE" switch to the "EXT." position, the "METER/MONITOR" switch to "OFF" and connect an oscilloscope between the junction of either R_{426} and R_{432} or R_{429} and R_{435} on the mixer board and ground. With the "REFERENCE LEVEL" control in the "1" position apply a sine wave of about 5 volts peak to peak to the "REFERENCE IN/OUT" terminals. Any convenient frequency between 15 cps and 15 KCS may be used. Under these conditions a square wave of about 1 volt peak to peak should be visible on the scope. Reduce the amplitude of the sine wave applied to the "REFERENCE IN/OUT" terminals until the Schmitt trigger just stops triggering. This is evidenced by the square wave disappearing from the scope. Increase the signal level until the Schmitt trigger circuit is just barely triggering. If the square wave is very asymmetrical under these circumstances, adjust the "SYMMETRY" control until it is reasonably symmetrical.

Now again reduce the level of the input signal until the Schmitt trigger just stops triggering. By varying the "HYSTERESIS" control it should be possible to get the Schmitt trigger to trigger once more. Keep reducing the signal level and readjusting the "HYSTERESIS" control until a point is reached at which the Schmitt trigger breaks into oscillation. Back off from this point until the oscillation disappears and then one turn further. Readjust the "SYMMETRY" adjust control so that this square wave looks reasonably symmetrical.

SYMMETRY CONTROL: Before making this adjustment the "HYSTERESIS" control should be set as outlined in the preceding section. To make this adjustment two audio-oscillators are required. When the duty cycle of the mixer switching waveform is exactly 50% the response of the mixer to the second (and all other even harmonics) of the reference frequency is a minimum. The "SYMMETRY CONTROL" is provided to so adjust the switching waveform. Although this adjustment could be made by observing this waveform on a good scope and setting the duty cycle to 50% as indicated by the scope, the following procedure is much to be preferred since it serves to minimize the mixer's response to the second harmonic. Set the "METER/MONITOR" switch in the "REF." position, the "REFERENCE MODE" switch in "EXT." and connect one of the audio-oscillators to the "REFERENCE IN/OUT" terminals. Set the oscillator frequency at about 1KC and adjust the "REFERENCE LEVEL" control and/or the level control on the oscillator until the meter indicates a normal reference channel level. Set the "FREQUENCY" and "FREQ. RANGE" controls to a frequency of 1KC. Set the second oscillator to a frequency of 1KC and connect it to the "SIGNAL INPUT" terminals. Switch the "METER/MONITOR"

switch to the "OUT X1" position and adjust the frequency of the second oscillator so that a slow beat is observed on the meter. Adjust the "FREQ. TRIM" control so that this beat has maximum amplitude. Now tune the oscillator connected to the "REFERENCE IN/OUT" terminals to about 500 cps. Set the "METER/MONITOR" switch on the "OUT X10" position and tune this oscillator slowly around 500 cps until a slow beat is observed on the meter. In general this beat will be of very small amplitude. Adjust the "SYMMETRY" control until the amplitude of the beat so observed is a minimum. This leads to a proper adjustment of the "SYMMETRY" control.

A.C. BALANCE CONTROL: To set this control, set the "REFERENCE MODE" switch in the "INT." position, the "FREQUENCY" and "FREQ. RANGE" controls to a frequency of about 400 cps, the "SIGNAL LEVEL" control to "OFF", and the "METER/MONITOR" switch to the "OFF" position. Connect the scope between either of the red "MONITOR" terminals and ground. If a balanced scope is available, its use is to be preferred and it should be connected between the two red "MONITOR" terminals. Adjust the "A.C. BALANCE" control until the amplitude of the square wave seen on the scope is a minimum. This completes the adjustment of the "A.C. BALANCE" control.

POWER SUPPLY: This board is located on the extreme left as viewed from the back of the instrument. It has only one adjustment which is used to set the voltage on the -24 line at -24. This is R_{511} on the schematic diagram. In making this adjustment, it is necessary to remove the bottom panel from the instrument in order to gain access to the connector into which the power supply board is plugged. Connect an accurate D.C. voltmeter between pin 15 on this connector and the chassis. Pin 15 is the terminal on the connector closest to the bottom of the instrument.

The meter should read 24 volts. If it does not, adjust R_{511} until the meter reads exactly 24 volts. Under these circumstances the voltage on pin 1 of the same connector should read -20 $\pm$ 2 volts. If the voltage read on pin 1 is outside of these limits, it indicates trouble in the 20 volt regulator which should be checked.

INTERNAL OSCILLATOR ADJUSTMENT: It is necessary to remove the top cover from the instrument in order to make this adjustment. The Trimpot involved is mounted on the top edge of the printed circuit board which is mounted between the front panel and the panel carrying the connectors for the plug-in printed circuit boards. Before making this adjustment the Q of the reference amplifier should be set to 25. If this has not been done, this adjustment should be checked. See the section on the "REFERENCE CHANNEL TUNED AMPLIFIER" adjustment for instructions on doing this. The internal oscillator adjustment is made as follows: set the "REFERENCE MODE" switch on "INT.", the "REFERENCE LEVEL" control on "1" and the "FREQUENCY" and "FREQ. RANGE" dials to a frequency of 400 cps. Connect an oscilloscope across the "REFERENCE IN/OUT" terminals. A sine wave should appear on the scope. Adjust the "OSCILLATOR LEVEL" control until the amplitude of this sine wave is 5 volts peak to peak, as indicated by the scope. The "OSCILLATOR LEVEL" control is now properly set.

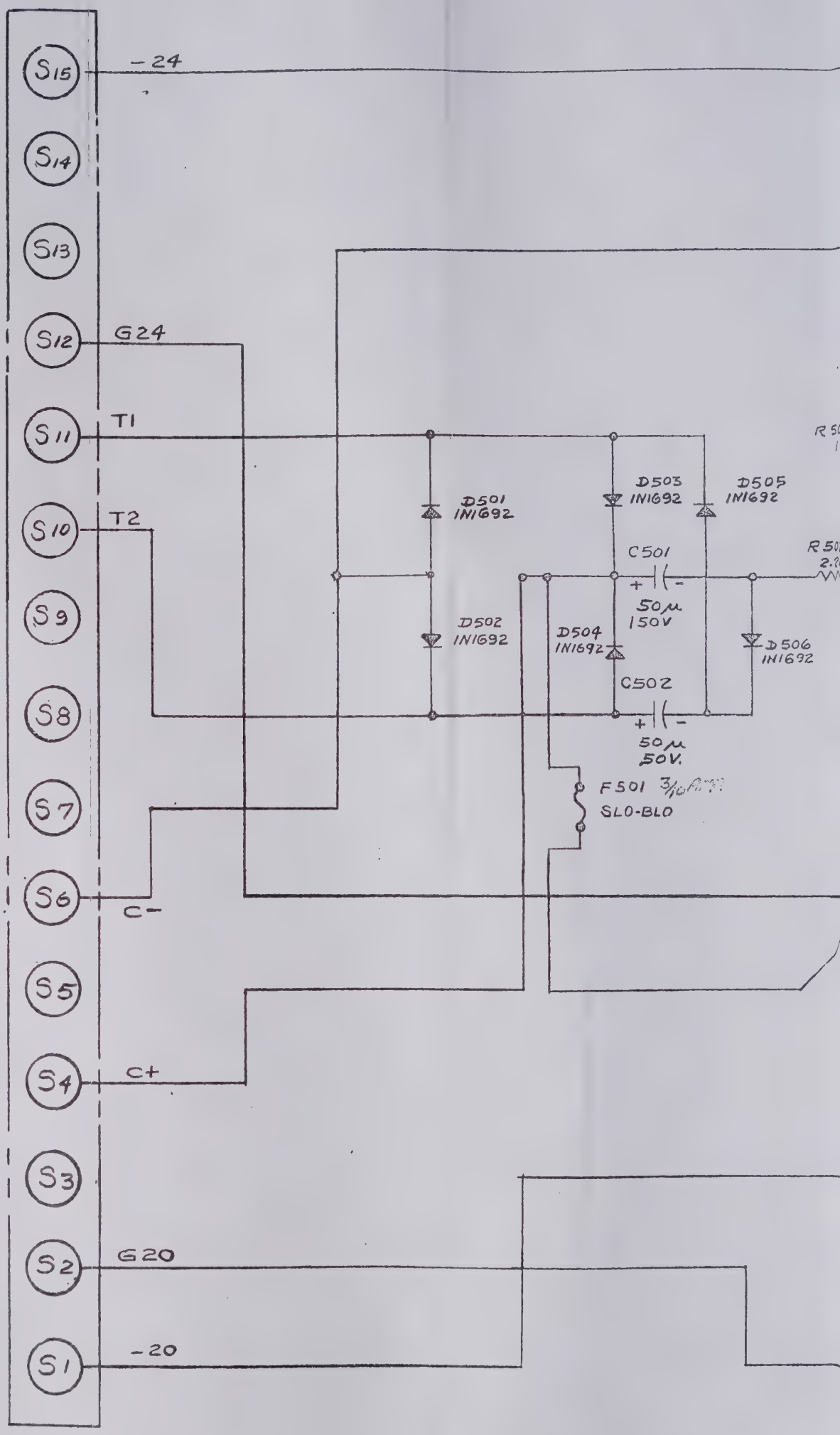
VI. WARRANTY

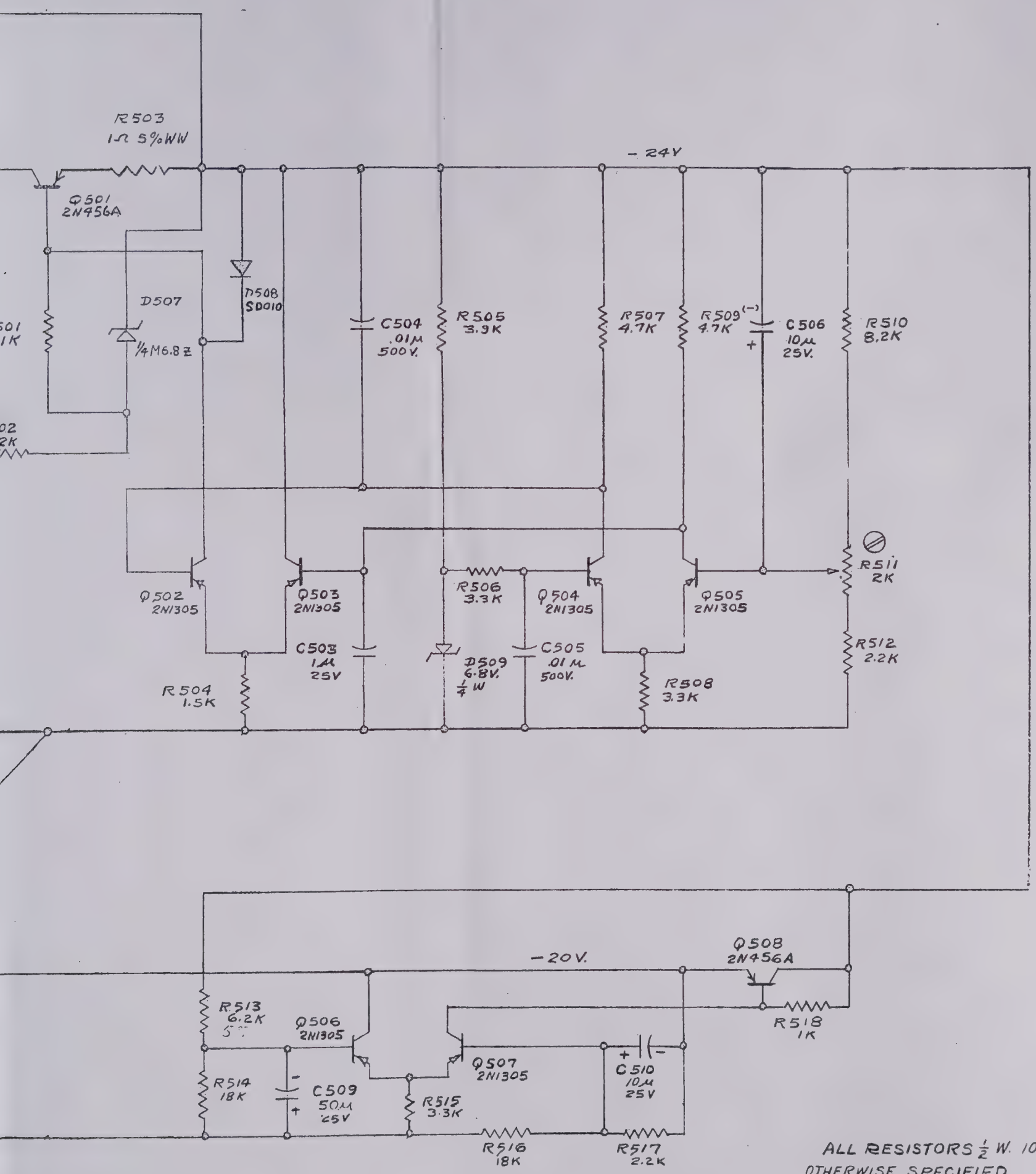
Princeton Applied Research Corporation warrants each instrument of its own manufacture to be free from defects in material and workmanship. Obligations under this Warranty are limited to replacing, repairing or giving credit for the purchase price, at our option, of any instrument returned prepaid to our factory for that purpose within ONE YEAR of delivery to the original purchaser. Instruments returned to the factory are accepted only when prior authorization has been given by an authorized representative of Princeton Applied Research Corporation.

This Warranty shall not apply to any instrument, which our inspection shall disclose to our satisfaction, to have become defective or unworkable due to abuse, mishandling, misuse, accident, alteration, negligence, improper installation, or other causes beyond our control. (Tubes, semiconductors, choppers, and lamp bulbs are exempted from this Warranty).

Princeton Applied Research Corporation reserves the right to make changes in design at any time without incurring any obligation to install same on units previously purchased.

This Warranty is in lieu of, and excludes all other warranties or representations, expressed, implied or statutory, and all other obligations or liabilities of Princeton Applied Research Corporation including special or consequential damages, and no other source is authorized to assume for Princeton Applied Research Corporation any other liability.


S15
-24
S14
S13
S12
G24
S11
T1
S10
T2
S9
S8
S7
S6
C-
S5
S4
C+
S3
S2
G20
S1
-20
D501
IN1692
D502
IN1692
D503
IN1692
D504
IN1692
D505
IN1692
D506
IN1692
C501
50μ
150V
C502
50μ
50V.
F501
SLO-BLO



ALL RESISTORS 1/2 W. 10% UNLESS OTHERWISE SPECIFIED

JUN 25 1963

NEXT ASSEM.

LAST "R" NO USED R518
" "C" " " C510
" " " " Q508
" D " " D509
C507 OR C508

NO.	ZONE	REVISION DY	CHKD.	APPD.	DATE
A		R.D.M.	R.D.M.	R.D.M.	5/10/62

CHANGED D508 TO SD010

R501 WAS ½ AMP

DSGN: RD MOORE	
DRAWN: WJL	1/9/62
CHKD: R.D.M.	4/23/62
APPD: R.D.M.	4/23/62
SCALE: dp	
TOLERANCES UNLESS NOTED NON-ACCUMULATIVE	
FRACTIONS ±	
DECIMALS ±	
ANGULAR ±	

PRINCETON APPLIED RESEARCH CORPORATION
PRINCETON, NEW JERSEY

TITLE: POWER BOARD SCHEMATIC

USED IN MODELS: JB-4, JB-5

DWG. NO. 0104-C-SD

REV B

SIGNAL CHANNEL TUNED AMPL.

REFERENCE TUNED AMPLIFIER

PHASE SHIFTER & VOLTMETER

MIXER

POWER BOARD

IN

OUT

SIG IN

PH SHIFT IN

SCR

INPUT

SIGNAL LEVEL

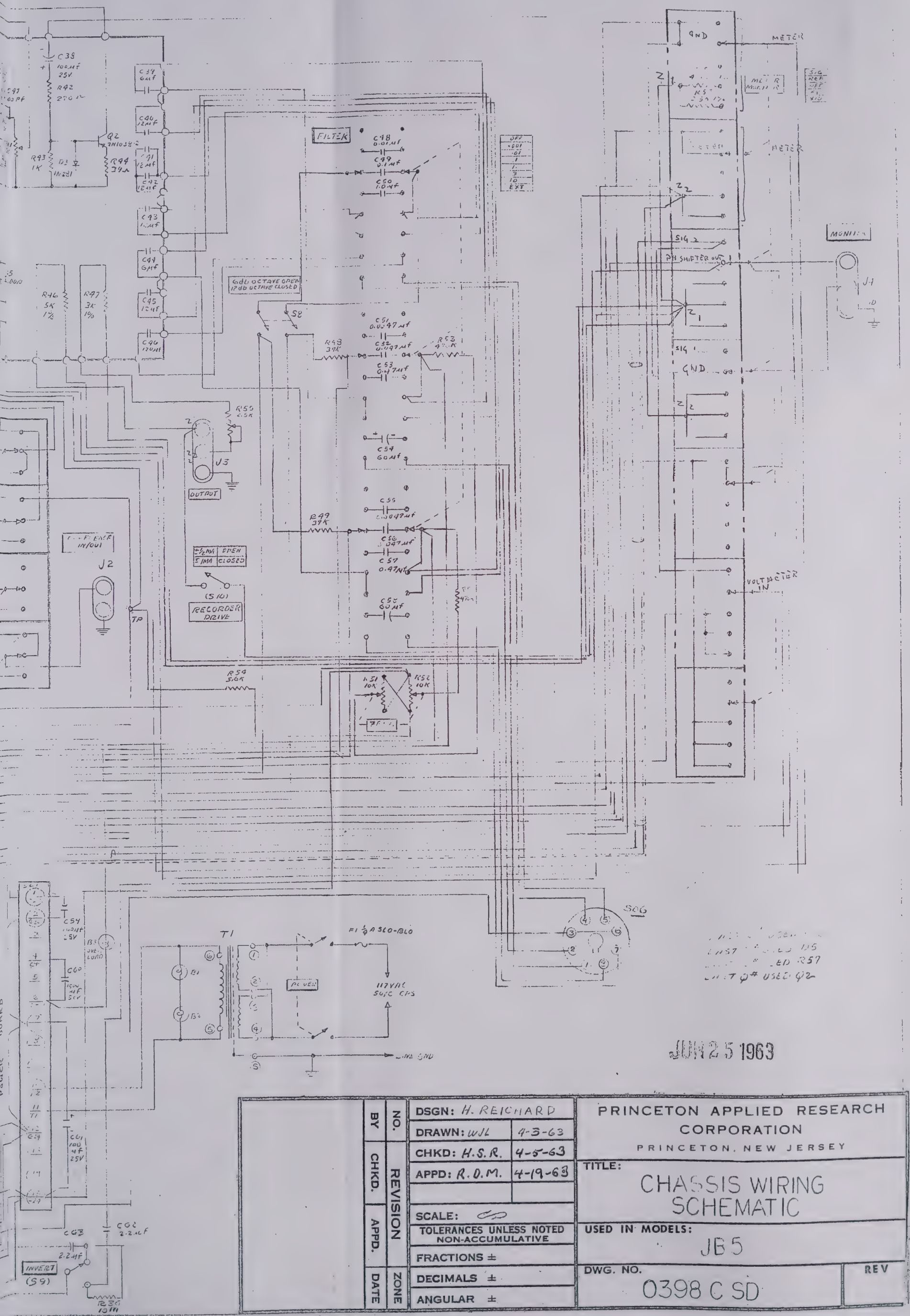
REFERENCE LEVEL

PHASE

FREQUENCY

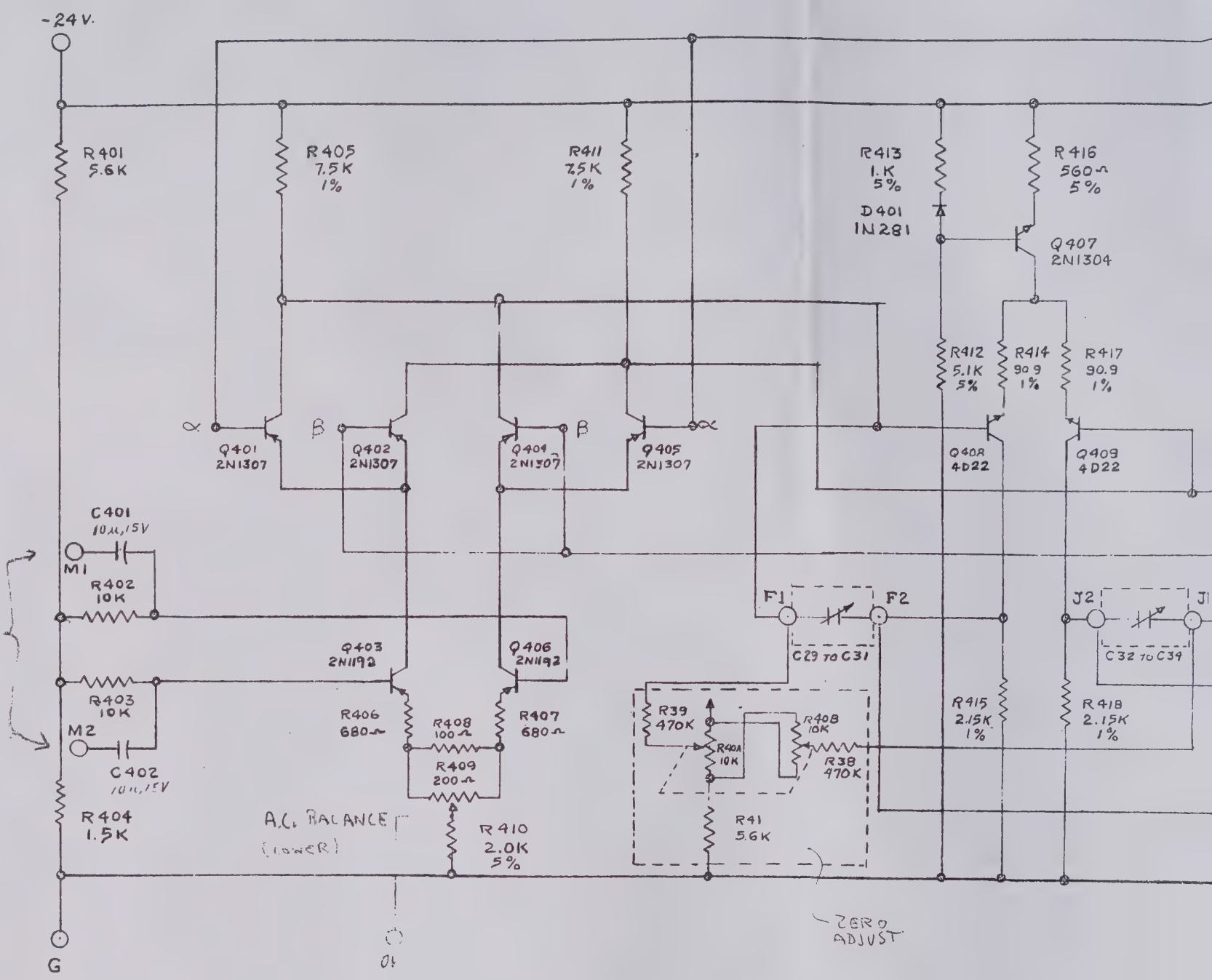
REFERENCE MODE SW

EXT. SEL. INT



JUN 25 1963

	NO.	DSGN: H. REICHARD	PRINCETON APPLIED RESEARCH CORPORATION PRINCETON, NEW JERSEY
	BY	DRAWN: WJL 4-3-63	
	CHKD.	CHKD: H.S.R. 4-5-63	TITLE: CHASSIS WIRING SCHEMATIC
	APPD.	APPD: R.D.M. 4-19-63	USED IN MODELS: JB 5
	DATE	SCALE:	DWG. NO. 0398 C SD
	ZONE	TOLERANCES UNLESS NOTED NON-ACCUMULATIVE	REV
	REVISION	FRACTIONS ±	
		DECIMALS ±	
		ANGULAR ±	

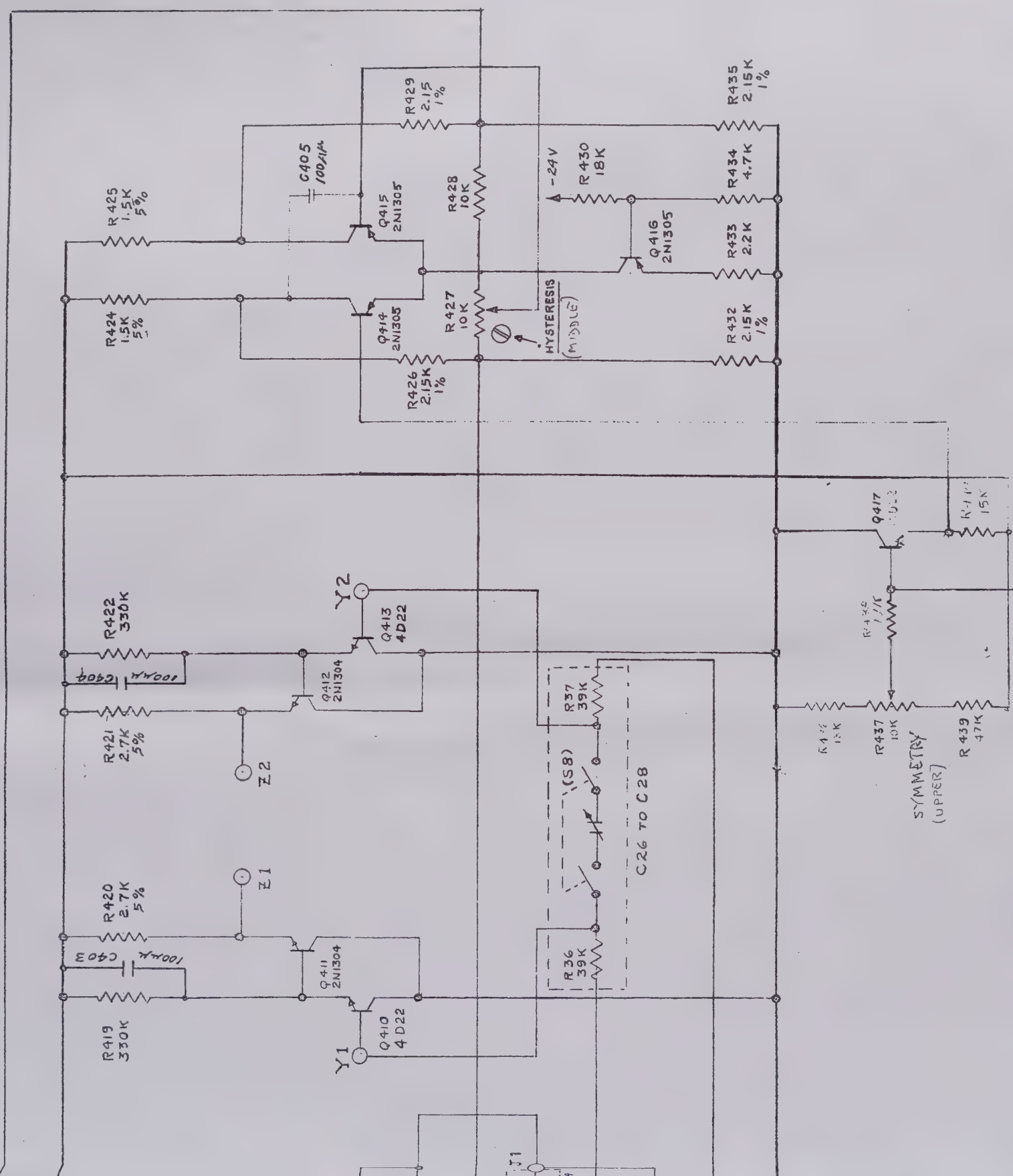


1 -24	2 X	3	4 Y2	5 Z2	6 Z1	7 Y1	8 GND	9 0V	10 J2	11 F2	12 F1	13 J1	14 M2	15 M1

PIN CONNECTIONS

ALL RESISTORS $\frac{1}{2}$ W
OTHERWISE SPECIFIED.

LAS
LAS
LAS
LAS



JUN 25 1963

NEXT ASSEM.

PRINCETON APPLIED RESEARCH CORPORATION
PRINCETON, NEW JERSEY

TITLE:
MIXER DC AMPLIFIER
SCHEMATIC DIAGRAM

USED IN MODELS:
JB-5

DWG. NO. 03000 C D

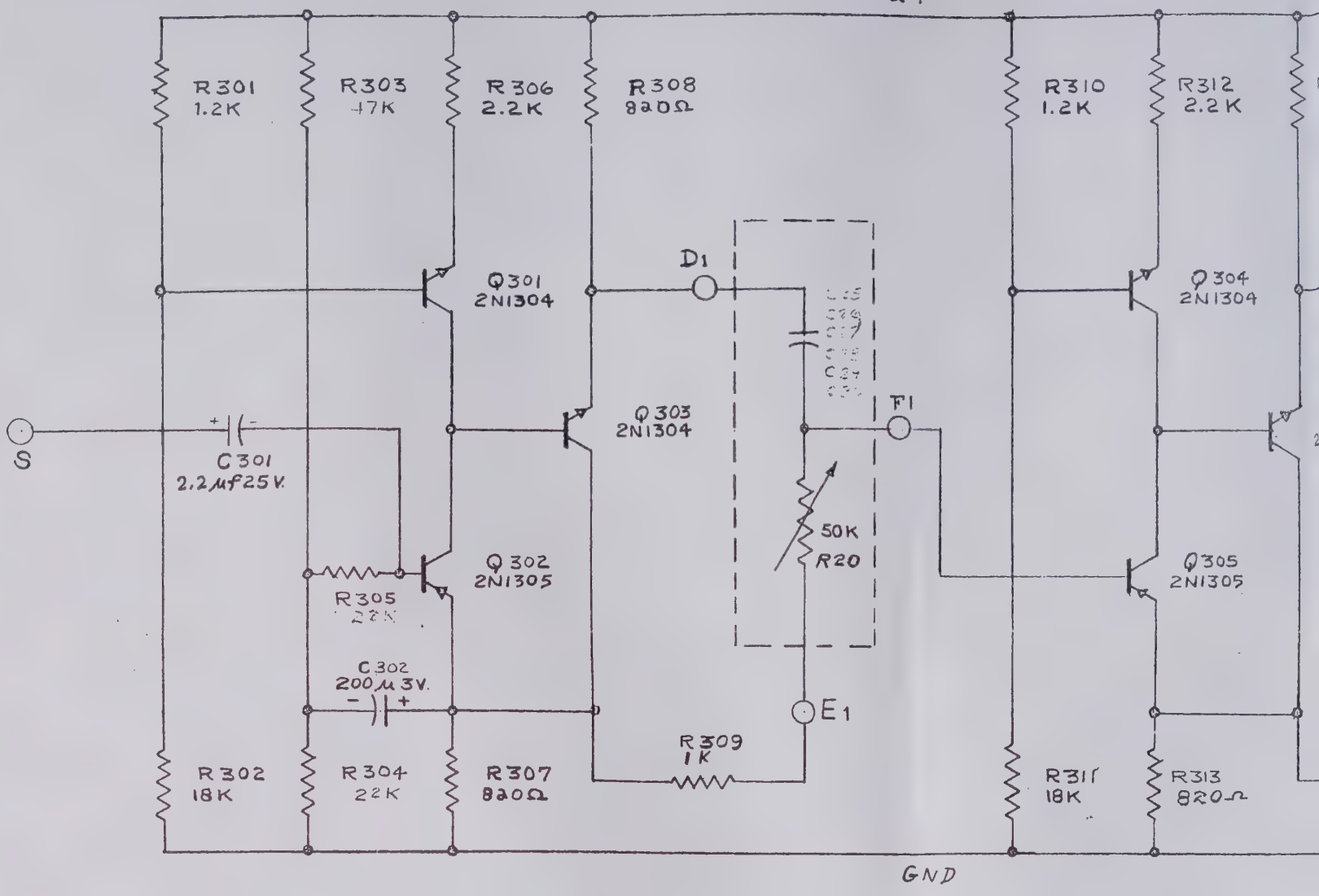
REV A

DSGN: H. REICHARD	
DRAWN: WJL	4/19/63
CHKD: R.D.M.	
APPD: R.D.M.	
SCALE: CD	
TOLERANCES UNLESS NOTED NON-ACCUMULATIVE	
FRACTIONS ±	
DECIMALS ±	
ANGULAR ±	

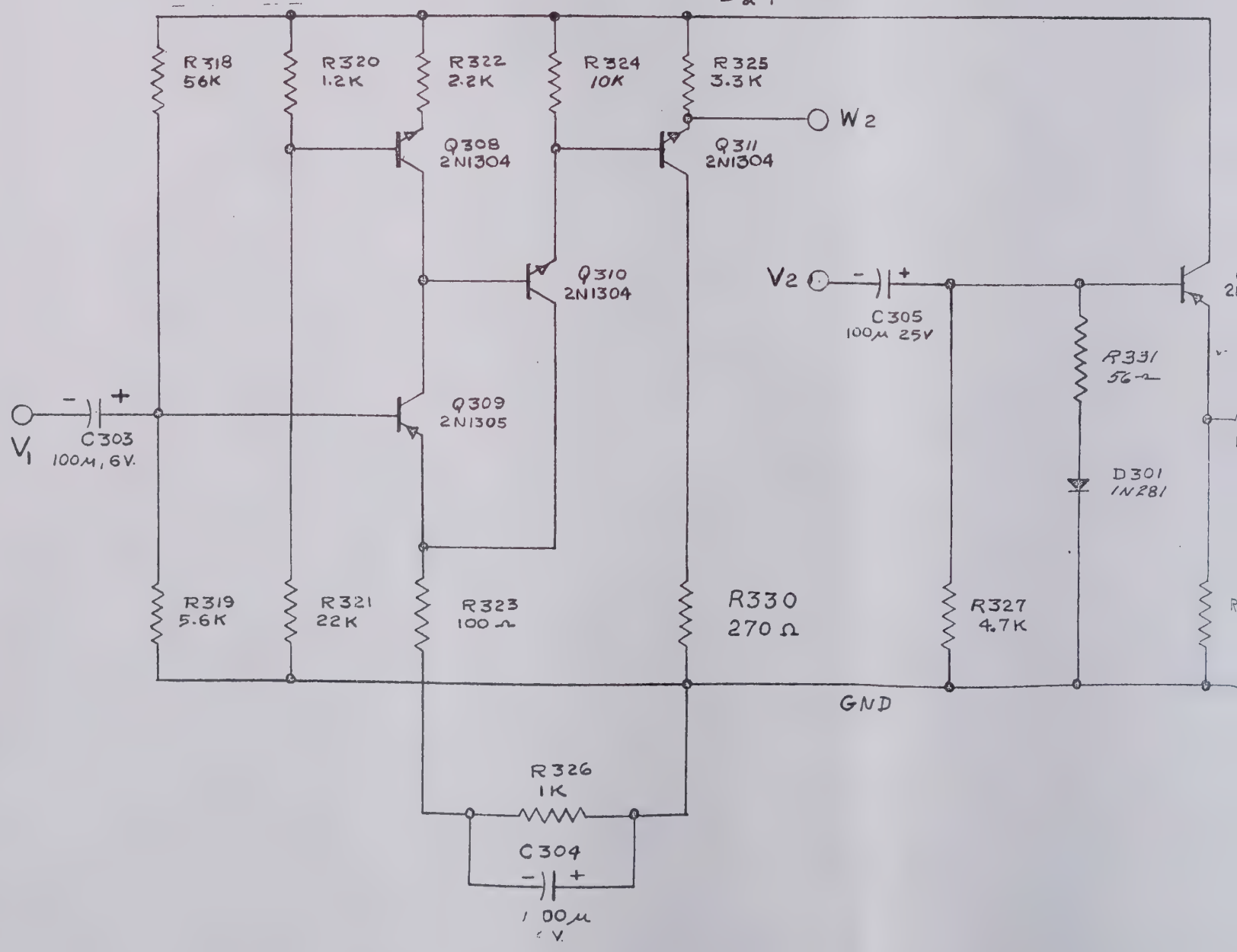
NO.	REVISION		ZONE
BY	CHKD.	APPD.	DATE
A	CHANGE Q403,C TO 2N1192		

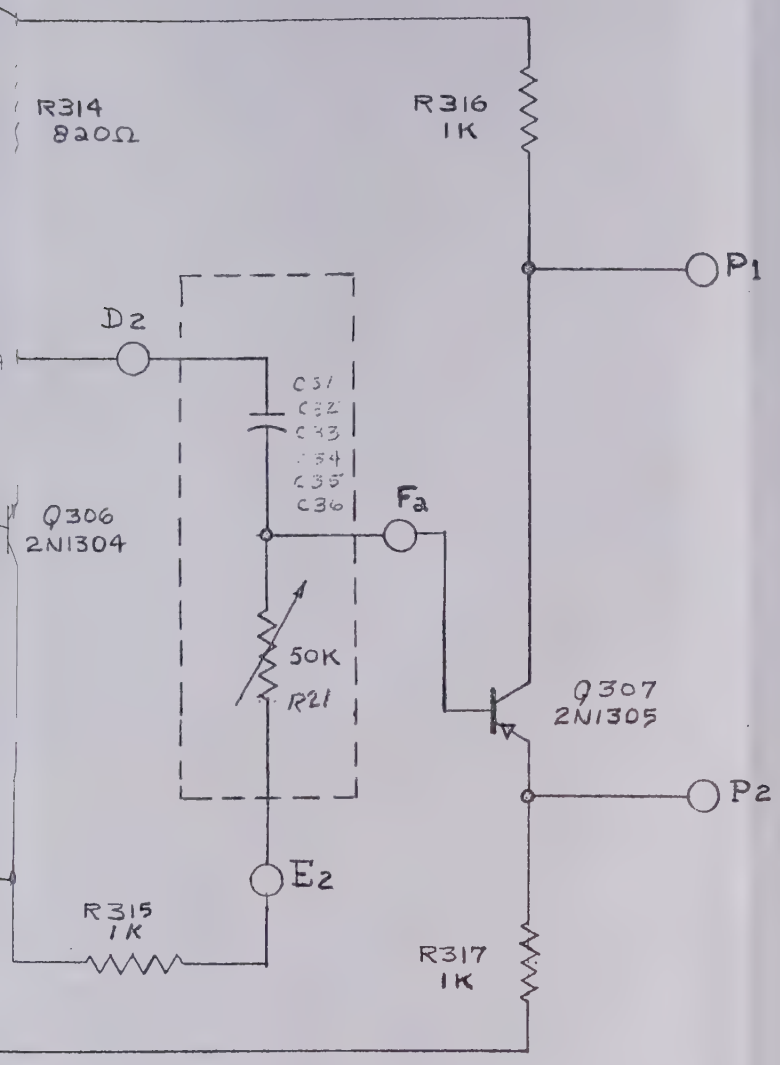
UNLESS OTHERWISE SPECIFIED RESISTORS ARE ±10% UNLESS NOTED. CAPACITORS ARE IN µf UNLESS NOTED.

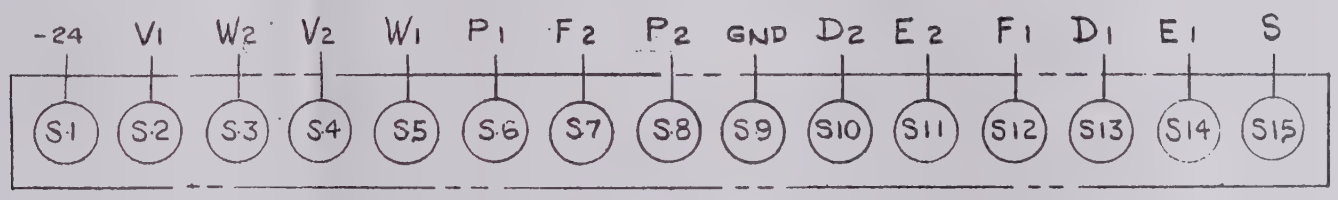
LAST C Nº USED C405
LAST D Nº USED D401
LAST R Nº USED R440
LAST Q Nº USED Q417


R301
1.2K
R303
47K
R306
2.2K
R308
820Ω
R310
1.2K
R312
2.2K
D1
Q301
2N1304
Q304
2N1304
Q303
2N1304
F1
S
C301
2.2μf25V.
50K
R20
Q302
2N1305
Q305
2N1305
R305
22K
C302
200μ3V.
E1
R309
1K
R302
18K
R304
22K
R307
820Ω
R311
18K
R313
820Ω
GND

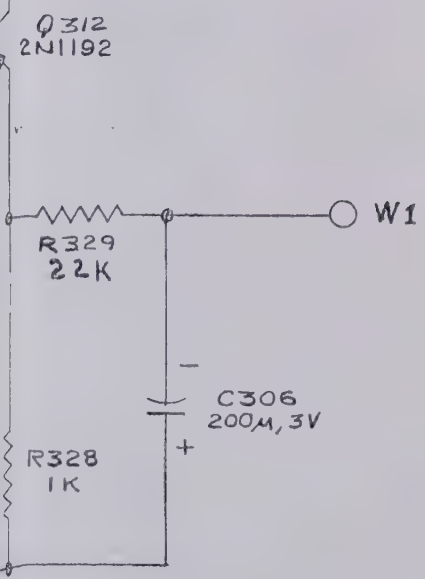
-24


R318
56K
R320
1.2K
R322
2.2K
R324
10K
R325
3.3K
W2
Q308
2N1304
Q311
2N1304
Q310
2N1304
V2
C305
100μ 25V
R331
56Ω
Q309
2N1305
V1
C303
100μ, 6V.
D301
IN281
R319
5.6K
R321
22K
R323
100Ω
R330
270Ω
R327
4.7K
GND
R326
1K
C304
100μ
V.





PIN CONNECTIONS
(S03)



ALL RESISTORS ½W 10% UNLESS OTHERWISE SPECIFIED.

LAST C Nº USED C306
LAST D Nº USED D301
LAST R Nº USED R331
LAST Q Nº USED Q312

JUN 25 1963

NEXT ASSEM.

REVISION NO.	BY	CHKD.	APPD.	DATE	ZONE
A	AMM	H.B.	R.D.M.	6-24-63	
U 0301 WAS IN 1692					

DSGN: R D MOORE
DRAWN: W J L
CHKD: R.D.M. 4-19-63
APPD: R.D.M. 4-19-63
SCALE:
TOLERANCES UNLESS NOTED NON-ACCUMULATIVE
FRACTIONS ±
DECIMALS ±
ANGULAR ±

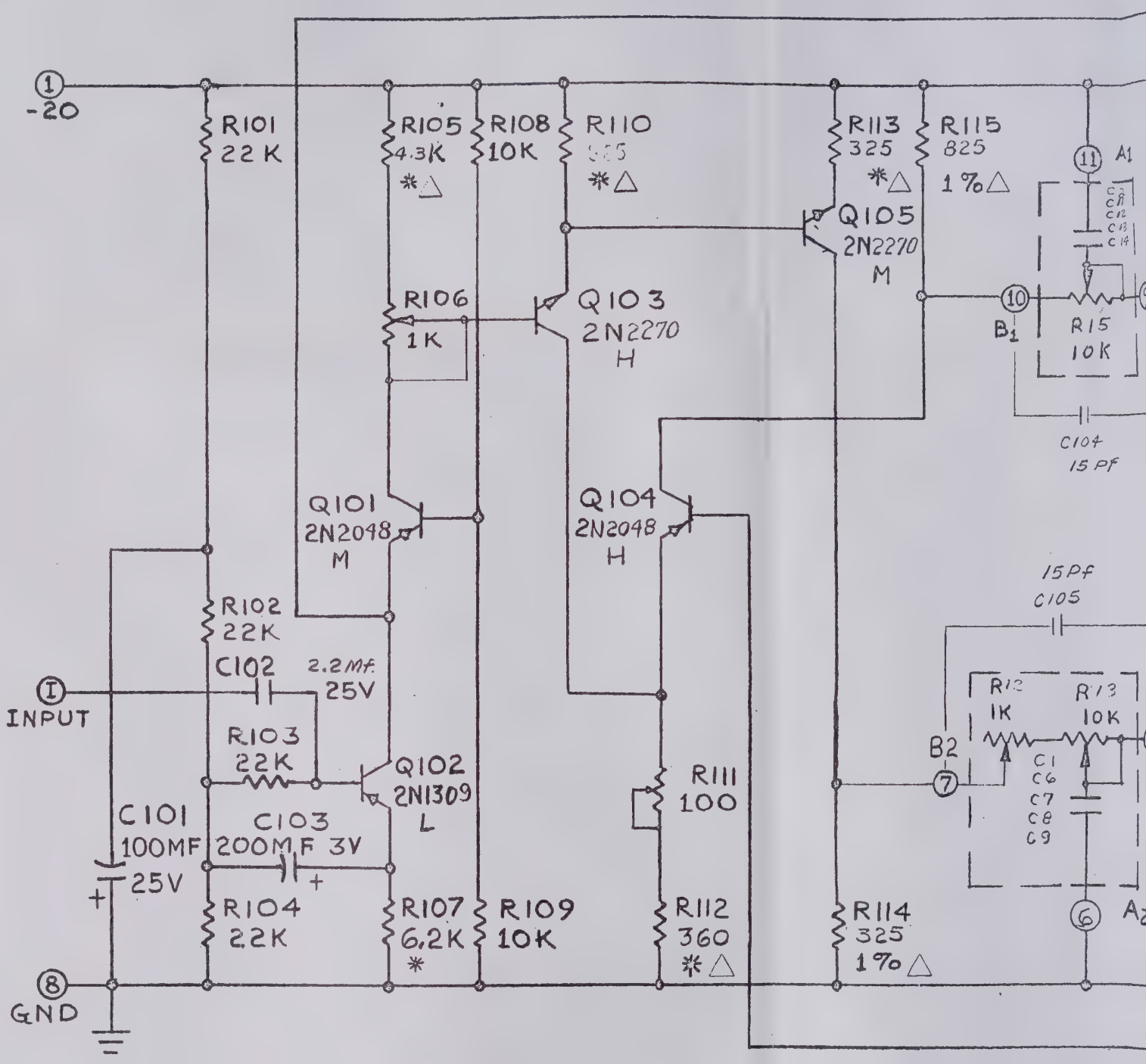
PRINCETON APPLIED RESEARCH CORPORATION
PRINCETON NEW JERSEY

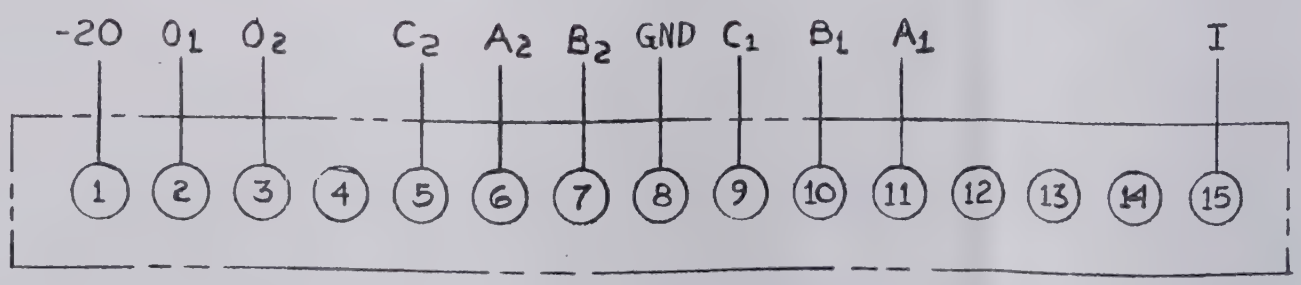
TITLE:
PHASE SHIFTER & VOLTMETER
SCHEMATIC DIAGRAM

USED IN MODELS:
JB-5

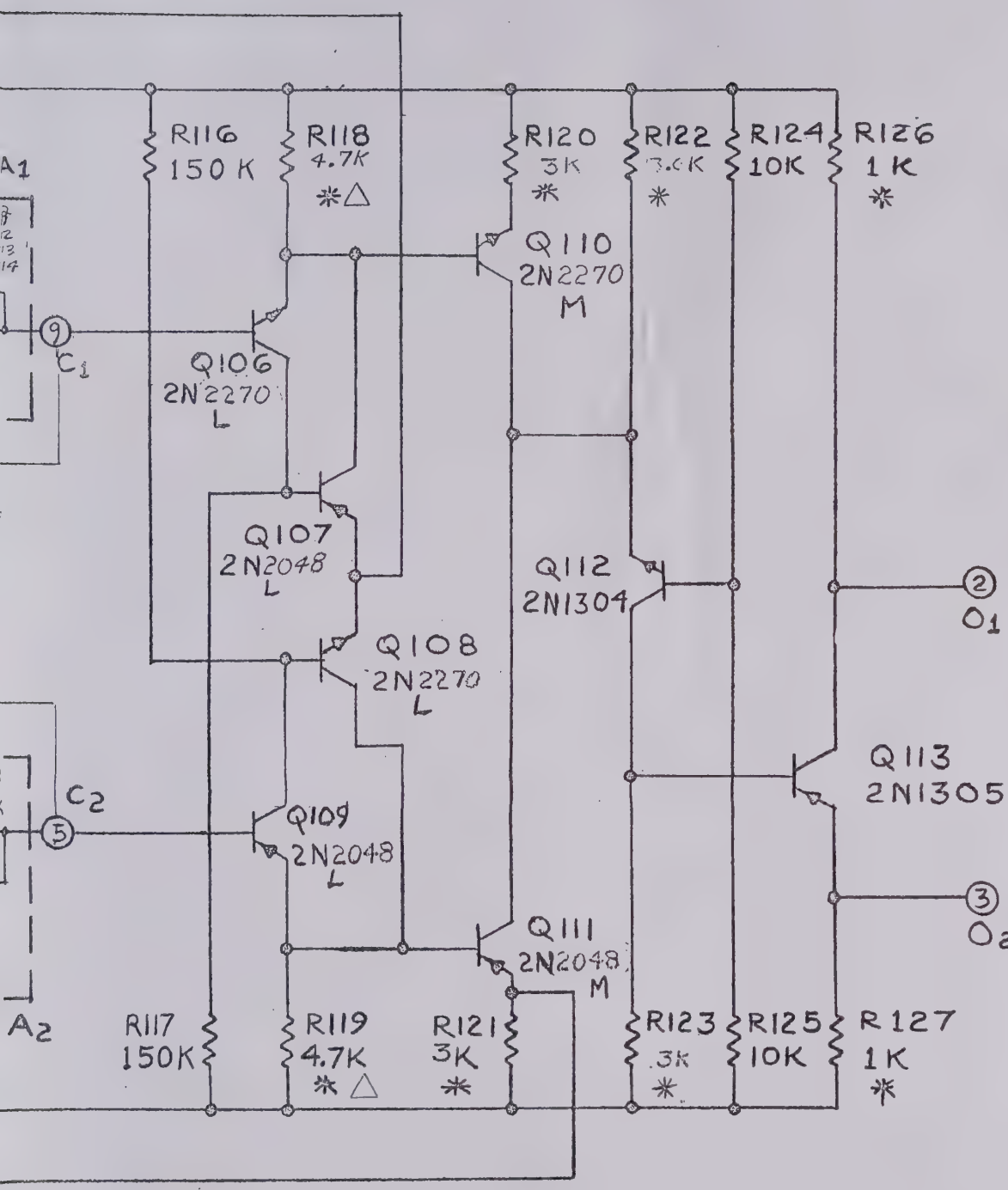
DWG. NO. 0400 C SD

REV A





PIN CONNECTIONS
S05



NOTE:

1. ALL RESISTORS ½W 10% UNLESS OTHERWISE SPECIFIED.
2. * RESISTORS MARKED WITH ASTERISK ARE 5%
3. LAST RESISTOR No. USED R 127
4. LAST TRANSISTOR No. USED Q 113
5. LAST CAPACITOR No. USED C 105
6. △ RESISTORS SO MARKED ARE HIGH STABILITY TYPE

H - TO HAVE $\beta \geq 200$ AT 5 mA

M - TO HAVE $\beta \geq 150$ AT 5 mA

L - TO HAVE $\beta \geq 100$ AT 5 mA

JUN 25 1963

NEXT ASSEM.

BY	NO.	REVISION	DATE
CHKD.			ZONE
APPD.			

DSGN: H. REICHARD

DRAWN W.J.L 4-8-63

CHKD: H.S.R. 4-8-63

APPD: R.D.M. 4-19-63

SCALE: ~

TOLERANCES UNLESS NOTED NON-ACCUMULATIVE

FRACTIONS ± ~

DECIMALS ± ~

ANGULAR ± ~

PRINCETON APPLIED RESEARCH CORPORATION

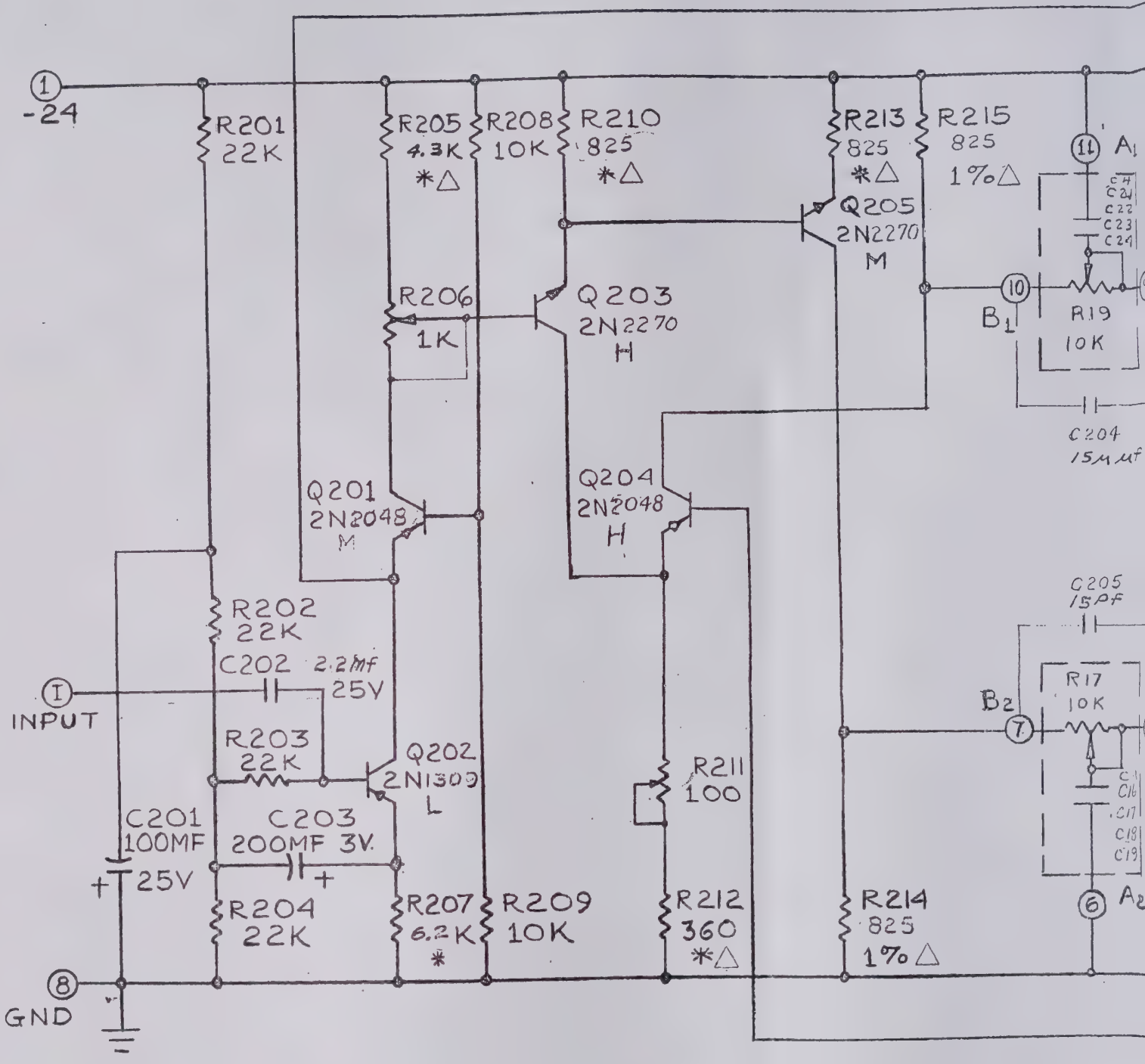
PRINCETON, NEW JERSEY

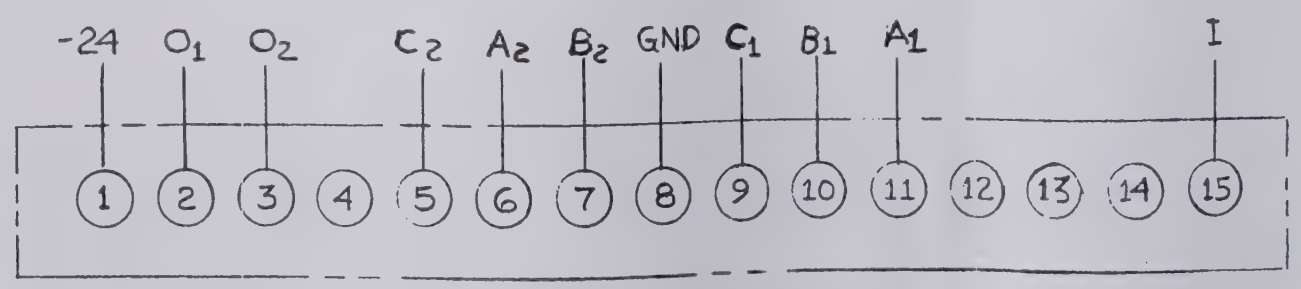
TITLE: SIGNAL CHANNEL TUNED AMPLIFIER SCHEMATIC

USED IN MODELS: JB-5

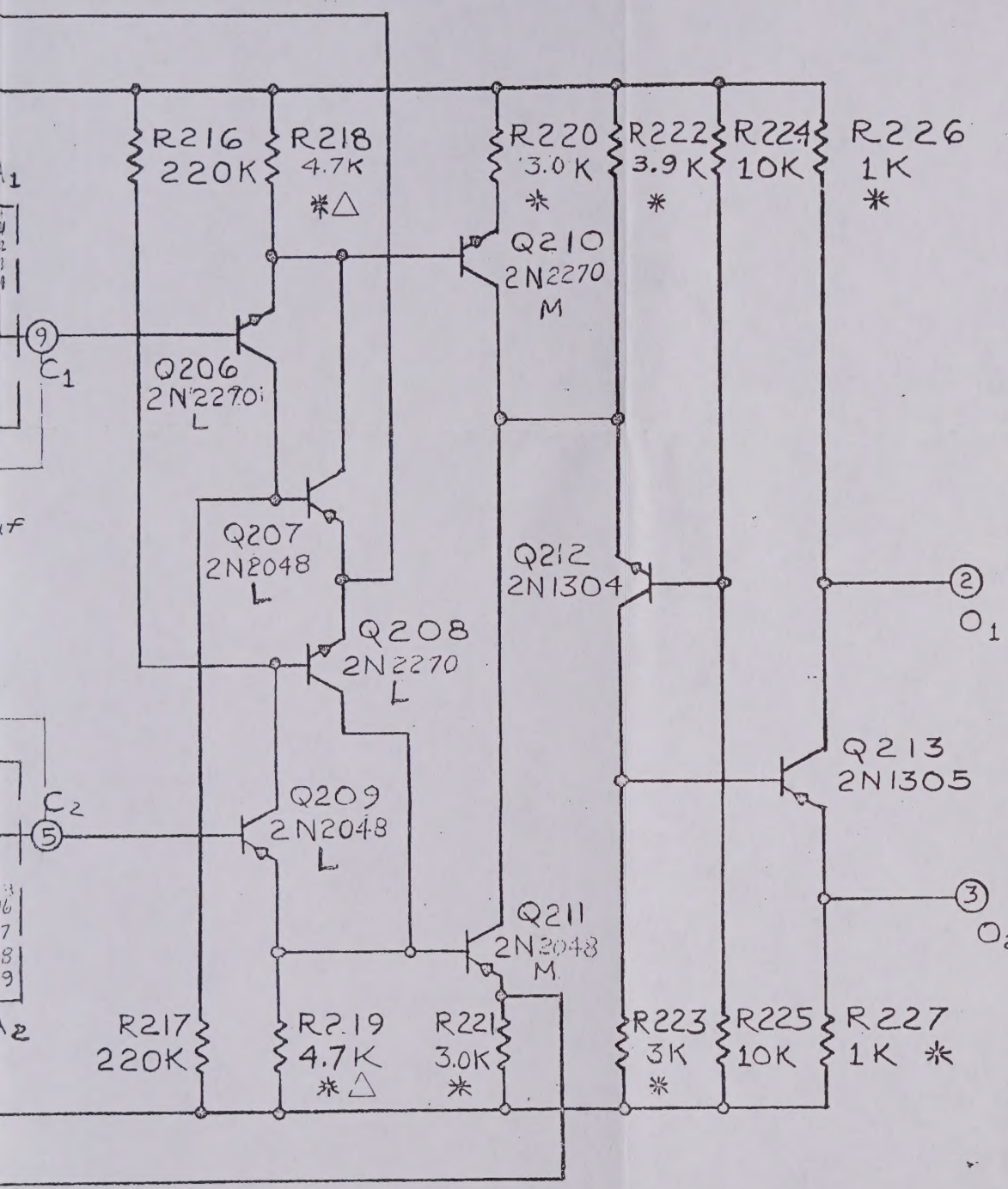
DWG. NO. 0403 C-SD

REV





PIN CONNECTIONS
SO4



NOTE:

1. ALL RESISTORS ½W 10% UNLESS OTHERWISE SPECIFIED.
2. * RESISTORS MARKED WITH ASTERISK ARE 5%.
3. LAST RESISTOR No. USED R227
4. LAST TRANSISTOR No. USED Q213
5. LAST CAPACITOR No. USED C205
6. △ RESISTORS SO MARKED ARE HIGH STABILITY TYPE

H-TO HAVE $\beta \geq 200$ AT 5 mA

M-TO HAVE $\beta \geq 150$ AT 5 mA

L-TO HAVE $\beta \geq 100$ AT 5 mA

JUN 25 1963

NEXT ASSEM.

BY	NO.	REVISION	DATE
CHKD.			ZONE
APPD.			

DSGN: H. REICHARD

DRAWN: W.J.L. 4-8-63

CHKD: H.S.R 4-8-63

APPD: R.D.M. 4-19-63.

SCALE: ~

TOLERANCES UNLESS NOTED NON-ACCUMULATIVE

FRACTIONS ± ~

DECIMALS ± ~

ANGULAR ± ~

PRINCETON APPLIED RESEARCH CORPORATION
PRINCETON, NEW JERSEY

TITLE: REFERENCE CHANNEL TUNED AMPLIFIER SCHEMATIC

USED IN MODELS: JB-5

DWG. NO 0404 C SD

REV

MP MASTER
MA20032